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<b>UTILITY PATENT APPLICATION TRANSMITTAL</b> <small>(Only for new nonprovisional applications under 37 CFR 1.53(b))</small>	Attorney Docket No.	8733.20105
	First Inventor or Application Identifier	Jeom Jae KIM et al.
	Title	MULTI-DOMAIN LIQUID CRYSTAL DISPLAY DEVICE

<b>APPLICATION ELEMENTS</b> <small>See MPEP chapter 600 concerning utility patent application contents</small>	<b>ADDRESS TO:</b> Assistant Commissioner for Patents Box Patent Application Washington, DC 20231
1. <input checked="" type="checkbox"/> <b>Fee Transmittal Form (e.g. PTO/SB/17)</b> (Submit an original and a duplicate for fee processing)	<b>ACCOMPANYING APPLICATION PARTS</b>  6. <input type="checkbox"/> Assignment Papers (cover sheet & document(s)) 7. <input type="checkbox"/> 37 C.F.R. §3.73(b) Statement <input type="checkbox"/> Power of Attorney (when there is an assignee) 8. <input type="checkbox"/> English Translation Document (if applicable) 9. <input type="checkbox"/> Information Disclosure Statement (IDS)/PTO-1449 <input type="checkbox"/> Copies of IDS Citations 10. <input type="checkbox"/> Preliminary Amendment 11. <input checked="" type="checkbox"/> White Advance Serial No. Postcard 12. <input type="checkbox"/> Small Entity Statement(s) <input type="checkbox"/> Statement filed in prior application. Status still proper and desired. 13. <input checked="" type="checkbox"/> Certified Copy of Priority Document(s) (if foreign priority is claimed) 14. <input checked="" type="checkbox"/> Other: Check for \$1,234.00 Request for Priority (1 pg.)
2. <input checked="" type="checkbox"/> <b>Specification</b> Total Pages <b>44</b>	
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4. <input type="checkbox"/> <b>Oath or Declaration</b> Total Pages <input type="text"/> a. <input type="checkbox"/> Newly executed (original or copy) b. <input type="checkbox"/> Copy from a prior application (37 C.F.R. §1.63(d)) (for continuation/divisional with box 15 completed) i. <input type="checkbox"/> <b>DELETION OF INVENTOR(S)</b> Signed statement attached deleting inventor(s) named in the prior application, see 37 C.F.R. §1.63(d)(2) and 1.33(b).	
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15. If a CONTINUING APPLICATION, check appropriate box, and supply the requisite information below. <input type="checkbox"/> Continuation <input type="checkbox"/> Divisional <input checked="" type="checkbox"/> Continuation-in-part (CIP) of prior application no.: 09/256,180 Prior application information: Examiner: TBD Group Art Unit: 2871	
16. Amend the specification by inserting before the first line the sentence: <input type="checkbox"/> This application is a <input type="checkbox"/> Continuation <input type="checkbox"/> Division <input type="checkbox"/> Continuation-in-part (CIP) of application Serial No. Filed on <input type="checkbox"/> This application claims priority of provisional application Serial No. Filed	
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Re: Serial No.: New Continuation-In-Part Application  
Applicant(s): Jeom Jae KIM et al.  
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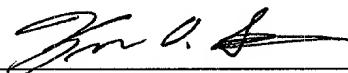
Attached hereto for filing are the following papers:

Transmittal Sheet  
Specification, Claims & Abstract (44 pgs.), Drawings (72 pgs.)  
Fee Transmittal Sheet  
Request For Priority  
Priority Documents (2)  
White Advance Serial Number Postcard

Our check in the amount of \$ 1,234.00 is attached covering any required fees. In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 C.F.R. §1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit the difference to our Deposit Account No. 50-0911. Further, if these papers are not considered timely filed, then a petition is hereby made under 37 C.F.R. §1.136 for the necessary extension of time. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

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PATENT

UNITED STATES PATENT APPLICATION

OF

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FOR

MULTI-DOMAIN LIQUID CRYSTAL DISPLAY DEVICE

**CROSS REFERENCE TO RELATED APPLICATIONS**

The application is a Continuation-in-Part of U. S. Patent Application Serial No. 09/256,180 filed on February 24, 1999 and this application also claims the benefit of Korean Patent Application No. 1999-23194, filed on June 21, 1999 and No. 1999-32945, filed on August 11, 1999, which are hereby incorporated by reference for all purposes as if fully set forth herein.

**BACKGROUND OF THE INVENTION****Field of the Invention**

The present invention relates to a liquid crystal display device (LCD), and more particularly, to a liquid crystal display device having a common-auxiliary electrode.

**Description of the Related Art**

Recently, an LCD has been proposed where the liquid crystal is not aligned and the liquid crystal is driven by side-electrodes insulated from pixel electrodes. Fig. 1 is sectional view of pixel unit of the conventional LCDs.

Regarding conventional LCDs, a plurality of gate bus lines arranged in a first direction on a first substrate and a plurality of data bus lines arranged in a second direction on the first substrate divide the first substrate into a

plurality of pixel regions. A thin film transistor (TFT) applies image signal delivered from the data bus line to a pixel electrode 13 on a passivation layer 37. The TFT is formed on each pixel region and comprises a gate electrode, a gate insulator, a semiconductor layer, a source electrode, and a drain electrode, etc. Side electrode 15 is formed surrounding the pixel electrode 13 on the gate insulator, thereon passivation layer 37 is formed over the whole first substrate, and a part of pixel electrode 13 overlaps side electrode 15.

Alternatively, pixel electrode 13 is formed on the gate insulator, and passivation layer 37 is formed over the whole first substrate.

On second substrate, a common electrode 17 is formed and together with pixel electrode 13 applies electric field to a liquid crystal layer. Side electrode 21 and open area (slit) 27 distort the electric field applied to the liquid crystal layer. Then in a unit pixel liquid crystal molecules are driven variously. This means that when voltage is applied to the LCD, dielectric energy due to the distorted electric field arranges the liquid crystal directors in needed or desired positions.

In the LCDs, however, open area 27 in common electrode 17 or pixel electrode 13 is necessary, and the liquid crystal

molecules could be driven stably when the open area is wider.

If the electrodes do not have an open area or the width of the open area is narrow, the electric field distortion needed to divide the pixel region becomes weak. Then, when voltage over a threshold voltage,  $V_{th}$ , is applied, the time needed to stabilize the liquid crystal directors increases. In particular, the response time can be over 100msec. At this time, disclination occurs from the area where the liquid crystal directors are parallel with a transmittance axis of the polarizer, which results in a decrease in brightness. Further, according to the surface state of LCDs, the liquid crystal texture has an irregular structure.

Furthermore, in the conventional LCDs, although a storage electrode is formed on the gate bus line to maintain the voltage applied to the pixel electrode, there are still the problems of filters resulted from the shortage generated between the gate bus line and the storage electrode.

#### **SUMMARY OF THE INVENTION**

Accordingly, the present invention is directed to an LCD that substantially obviates one or more problems due to limitations and disadvantages of the related art.

An object of the present invention is to provide a multi-domain LCD having high response time characteristics

and high brightness by stable arrangement of liquid crystal molecules, and preventing the flicker from generating.

Additional features and advantages of the invention will be set forth in the description which follows, and in part will be apparent from the description, or may be learned by practice of the invention. The objectives and other advantages of the invention will be realized and attained by the structure particularly pointed out in the written description and claims hereof as well as the appended drawings.

To achieve the objects and in accordance with the purpose of the invention, as embodied and broadly described herein, a multi-domain liquid crystal display device comprises first and second substrates facing each other; a liquid crystal layer between the first and second substrates; a plurality of gate bus lines arranged in a first direction on the first substrate and a plurality of data bus lines arranged in a second direction on the first substrate to define a plurality of pixel region; a thin film transistor positioned at a crossing area of the data bus line and the gate bus line, the thin film transistor comprising a gate electrode, a semiconductor layer, and source/drain electrodes; a common-auxiliary electrode comprising at least one electrode in the each pixel region; a plurality of pixel

electrodes electrically charged through the thin film transistor; and an alignment layer on at least one substrate between the first and second substrates.

It is to be understood that both the foregoing general description and the following detailed description are exemplary and explanatory and are intended to provide further explanation of the invention as claimed.

#### **BRIEF DESCRIPTION OF THE DRAWINGS**

The accompanying drawings, which are included to provide a further understanding of the invention and are incorporated in and constitute a part of the specification, illustrates embodiments of the invention and together with description serve to explain the principles of the invention.

In the drawings:

Fig. 1 is a sectional view of the liquid crystal display device in the related art;

Figs. 2A to 2C are plans of the multi-domain liquid crystal display devices according to the first embodiment of the present invention;

Fig. 3 is a sectional view taken along the line I-I of Fig. 2A;

Figs. 4A to 4B are sectional views taken along the line II-II of Fig. 2A



Fig. 5 is a sectional view taken along the line III-III of Fig. 2A;

Fig. 6 is a sectional view taken along the line IV-IV of Fig. 2A;

Fig. 7 is a sectional view taken along the line V-V of Fig. 2B;

Figs. 8A to 8C are plan views of the multi-domain liquid crystal display devices according to the second embodiment of the present invention;

Figs. 9A to 9C are plan views of the multi-domain liquid crystal display devices according to the third embodiment of the present invention;

Figs. 10A to 10C are plan views of the multi-domain liquid crystal display devices according to the fourth embodiment of the present invention;

Figs. 11A to 11C are plan views of the multi-domain liquid crystal display devices according to the fifth embodiment of the present invention;

Figs. 12A to 12C are plan views of the multi-domain liquid crystal display devices according to the sixth embodiment of the present invention;

Figs. 13A and 13B are plan views of the multi-domain liquid crystal display devices according to the seventh embodiment of the present invention;

Fig. 14 is a plan view of the multi-domain liquid crystal display devices according to the eighth embodiment of the present invention;

Fig. 15 is a sectional view taken along the line VI-VI of Fig. 14;

Fig. 16 is a plan view of the multi-domain liquid crystal display devices according to the ninth embodiment of the present invention;

Fig. 17 is a plan view of the multi-domain liquid crystal display devices according to the tenth embodiment of the present invention;

Fig. 18 is a plan view of the multi-domain liquid crystal display devices according to the eleventh embodiment of the present invention;

Fig. 19 is a plan view of the multi-domain liquid crystal display devices according to the twelfth embodiment of the present invention;

Fig. 20 is a plan view of the multi-domain liquid crystal display devices according to the thirteenth embodiment of the present invention;

Figs. 21A to 21C are plan views of the multi-domain liquid crystal display devices according to the fourteenth embodiment of the present invention;

Fig. 22 is a sectional view taken along the line VII-

VII of Fig. 21A;

Figs. 23A to 24D are sectional views taken along the line VIII-VIII of Fig. 21A;

Fig. 24 is a sectional view taken along the line IX-IX of Fig. 21B;

Figs. 25A to 25C are plan views of the multi-domain liquid crystal display devices according to the fifteenth embodiment of the present invention;

Figs. 26A to 26C are plan views of the multi-domain liquid crystal display devices according to the sixteenth embodiment of the present invention;

Figs. 27A to 27C are plan views of the multi-domain liquid crystal display devices according to the seventeenth embodiment of the present invention;

Figs. 28A to 28C are plan views of the multi-domain liquid crystal display devices according to the eighteenth embodiment of the present invention;

Figs. 29A to 29C are plan views of the multi-domain liquid crystal display devices according to the nineteenth embodiment of the present invention;

Figs. 30A and 30B are plan views of the multi-domain liquid crystal display devices according to the twentieth embodiment of the present invention;

Figs. 31A to 31E are views showing the forming process

of the present multi-domain liquid crystal display device taken along the line X-X of Fig. 2C;

Figs. 32A to 32G are plan views and sectional views taken along the lines I-I, II-II of Fig. 32A of the multi-domain liquid crystal display device according to the twenty-first embodiment of the present invention;

Figs. 33A to 33H are plan views and sectional views taken along the lines III-III of Fig. 33B and IV-IV of the Fig. 33A of the multi-domain liquid crystal display device according to the twenty-second embodiment of the present invention;

Figs. 34A and 34B are plan view and sectional view taken along the line V-V of Fig. 34A of the multi-domain liquid crystal display device according to the twenty-third embodiment of the present invention;

Figs. 35A and 35B are plan views and sectional views taken along the line VI-VI of Fig. 35B of the multi-domain liquid crystal display device according to the twenty-fourth embodiment of the present invention;

Figs. 36A to 36G are plan views showing the various windows inducing electric field and dielectric frames according to the embodiments of the present invention.

#### **DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS**

Hereinafter, the multi-domain liquid crystal display device of the present invention is explained in detail by accompanying the drawings.

Figs. 2A to 2C are plans of the multi-domain liquid crystal display devices according to the first embodiment of the present invention, Fig. 3 is a sectional view taken along the line I-I of Fig. 2A, Figs. 4A to 4B are sectional views taken along the line II-II of Fig. 2A, Fig. 5 is a sectional view taken along the line III-III of Fig. 2A, Fig. 6 is a sectional view taken along the line IV-IV of Fig. 2A, Fig. 7 is a sectional view taken along the line V-V of Fig. 2B.

As shown in the figures, the present invention comprises first and second substrates, a plurality of gate bus lines 1 arranged in a first direction on a first substrate and a plurality of data bus lines 3 arranged in a second direction on the first substrate, a common-auxiliary electrode 15, a TFT, a passivation layer 37, and a pixel electrode 13.

Data bus lines 3 and gate bus lines 1 divide the first substrate into a plurality of pixel regions. The common-auxiliary electrode 15 comprises at least one electrode in a pixel region on the same layer whereon the gate bus line 1 is formed. The TFT is formed on each pixel region and comprises a gate electrode 11, a gate insulator 35, a semiconductor layer 5, an ohmic contact layer 6, and source/drain

electrodes 7, 9. Passivation layer 37 is formed on the whole first substrate 31. Pixel electrode 13 is coupled to drain electrode 9.

On a second substrate, a light shielding layer 25 is formed to shield the light leaked from gate and data bus lines 1, 3, and the TFT, a color filter layer 23 is formed on the light shielding layer, a common electrode 17 is formed on the color filter layer, and a liquid crystal layer is formed between the first and second substrates.

Figs. 31A to 31E are views showing the forming process of the present multi-domain liquid crystal display device taken along the line X-X of Fig. 2C.

To manufacture the multi-domain LCD of the present invention, in each pixel region on the first substrate 31, a TFT is formed comprising gate electrode 11, gate insulator 35, semiconductor layer 5, ohmic contact layer 6 and source/drain electrodes 7, 9. At this time, a plurality of gate bus lines 1 and a plurality of data bus lines 3 are formed to divide the first substrate 31 into a plurality of pixel regions.

Gate electrode 11 and gate bus line 1 are formed by sputtering and patterning a metal such as Al, Mo, Cr, Ta, Al alloy, etc. At this time, common-auxiliary electrode 15 is formed surrounding the pixel region. (refer to the Fig. 31A)

The common-auxiliary electrode 15 comprises at least one

electrode in each pixel region, the each electrode is driven independently one another. And, as shown in the Figs. 2A and 5, the distance A between the common-auxiliary electrodes is within  $7\mu\text{m}$ , and preferably 4 to  $7\mu\text{m}$  at the connecting parts which connect to a common-auxiliary electrode in a pixel region neighboring the pixel region.

The gate insulator 35 is formed by depositing  $\text{SiN}_x$  or  $\text{SiO}_x$  using PECVD thereon. Semiconductor layer 5 and the ohmic contact layer 6 are formed by depositing with PECVD (Plasma Enhanced Chemical Vapor Deposition) and patterning amorphous silicon (a-Si) and doped amorphous silicon ( $n^+$  a-Si), respectively. Also, gate insulator 35, amorphous silicon (a-Si), and doped amorphous silicon ( $n^+$  a-Si) are formed by PECVD and patterned. (refer to the Fig. 31B)

Data bus line 3 and source/drain electrodes 7, 9 are formed by sputtering and patterning a metal such as Al, Mo, Cr, Ta, Al alloy, etc. (refer to the Fig. 31C)

A storage electrode 43 is formed to overlap gate bus line 1 and/or common-auxiliary electrode 15 at the same time, the storage electrode 43 makes a storage capacitor with gate bus line 1 and/or common-auxiliary electrode 15.

Subsequently, passivation layer 37 is formed with BCB (BenzoCycloButene), acrylic resin, polyimide based material,

SiN<sub>x</sub> or SiO<sub>x</sub> on the whole first substrate. Pixel electrode 13 is formed by sputtering and patterning a metal such as ITO(indium tin oxide). (refer to the Fig. 31D)

A contact hole 39 is formed to connect the pixel electrode to the drain and storage electrodes by opening and patterning a part of the passivation layer on drain electrode 9. (refer to the Fig. 31E)

When common-auxiliary electrode 15 and gate bus line 1 are formed with the same material, the common-auxiliary and gate electrodes are simultaneously formed with one mask and connected electrically to common electrode 17, or it is possible to form them with different materials or double layer with additional masks.

On the second substrate 33, a light shielding layer 25 is formed to shield any light leakage from gate and data bus lines 1, 3, the TFT, and common-auxiliary electrode 15. A color filter layer 23 is formed R, G, B (red, green, blue) elements to alternate on the light shielding layer. A common electrode 17 is formed with ITO on the color filter layer, and a liquid crystal layer is formed by injecting liquid crystal between the first and second substrates.

The liquid crystal layer may include liquid crystal molecules having positive or negative dielectric anisotropy.

Also, the liquid crystal layer may include chirals dopants.



On at least one substrate between the first and second substrates, a dielectric frame 53 is formed by depositing photosensitive material in a region other than a region where the pixel electrode 13 is formed and patterning in various shapes using photolithography.

The dielectric frame 53 includes material of which dielectric constant is same or smaller than that of the liquid crystal, and the dielectric constant thereof is preferably below 3, for example, photoacrylate or BCB (BenzoCycloButene).

To apply voltage ( $V_{com}$ ) to common-auxiliary electrode 15, Ag-dotting part is formed in each corner of driving area on first substrate 31, electric field is applied with second substrate 33, and the liquid crystal molecules are driven by the potential different. A voltage ( $V_{com}$ ) is applied to common-auxiliary electrode 15 by connecting the Ag-dotting part to the common-auxiliary electrode, which is accomplished simultaneously by forming the common-auxiliary electrode.

On at least one substrate, a compensation film 29 is formed with polymer. The compensation film is a negative uniaxial film, which has one optical axis, and compensates the phase difference of the direction according to viewing-angle. Hence, it is possible to compensate effectively the right-left viewing-angle by widening the area without gray

inversion, increasing contrast ratio in an inclined direction, and forming one pixel to multi-domain.

In the present multi-domain liquid crystal display device, it is possible to form a negative biaxial film as the compensation film, which has two optical axes and wider viewing-angle characteristics as compared with the negative uniaxial film. The compensation film could be formed on both substrates or on one of them.

After forming the compensation film, polarizer is formed on at least one substrate. At this time, the compensation film and polarizer are preferably composed as one.

In the multi-domain liquid crystal display device of the present invention, the pixel electrode 13 overlaps common-auxiliary electrode 15, light shielding layer 25 overlaps the common-auxiliary electrode, and then the aperture ratio is improved. Storage electrode 43 forms a storage capacitor  $C_{ST}$  by overlapping gate bus line 1 and/or the common-auxiliary electrode 15.

In the multi-domain liquid crystal display device of the in the Fig. 2A, two common-auxiliary electrode 15 surround the pixel region and are driven independently in the pixel region. The each common-auxiliary electrode has connecting parts which connect to a common-auxiliary electrode in a pixel region neighboring the pixel region. A storage

electrode 43 is formed overlapping the common-auxiliary electrode 15 and the gate bus line 1 to obtain storage capacitance.

In the multi-domain liquid crystal display device of the in the Fig. 2B, two common-auxiliary electrode 15 surround the pixel region and are driven independently in the pixel region. The each common-auxiliary electrode has connecting parts which connect to a common-auxiliary electrode in a pixel region neighboring the pixel region. A storage electrode 43 is formed overlapping the common-auxiliary electrode 15, the gate bus line 1, and a common-auxiliary electrode in a pixel region neighboring the pixel region to obtain storage capacitance.

In the multi-domain liquid crystal display device of the in the Fig. 2C, two common-auxiliary electrode 15 surround the pixel region and are driven independently in the pixel region. The each common-auxiliary electrode has connecting parts which connect to a common-auxiliary electrode in a pixel region neighboring the pixel region. A storage electrode 43 is formed overlapping the common-auxiliary electrode 15 and a common-auxiliary electrode in a pixel region neighboring the pixel region to obtain storage capacitance and to remove the signal delay of the bus lines.

Figs. 8A to 8C are plan views of the multi-domain liquid

crystal display devices according to the second embodiment of the present invention, Figs. 9A to 9C are plan views of the multi-domain liquid crystal display devices according to the third embodiment of the present invention, Figs. 10A to 10C are plan views of the multi-domain liquid crystal display devices according to the fourth embodiment of the present invention, Figs. 11A to 11C are plan views of the multi-domain liquid crystal display devices according to the fifth embodiment of the present invention, Figs. 12A to 12C are plan views of the multi-domain liquid crystal display devices according to the sixth embodiment of the present invention, Figs. 13A and 13B are plan views of the multi-domain liquid crystal display devices according to the seventh embodiment of the present invention.

In the multi-domain liquid crystal display device of the in the Fig. 8, two common-auxiliary electrode 15 surround the pixel region and are driven independently in the pixel region.

The each common-auxiliary electrode has connecting parts which connect to a common-auxiliary electrode in a pixel region neighboring the pixel region, and the connecting part has protrusions.

A storage electrode 43 is formed overlapping the common-auxiliary electrode 15 and the gate bus line 1 (Fig. 8A), the common-auxiliary electrode 15, the gate bus line 1, and a

common-auxiliary electrode in a pixel region neighboring the pixel region (Fig. 8B), the common-auxiliary electrode 15 and a common-auxiliary electrode in a pixel region neighboring the pixel region (Fig. 8C) to obtain storage capacitance.

In the multi-domain liquid crystal display device of the in the Fig. 9, two common-auxiliary electrode 15 surround the pixel region and are driven independently in the pixel region.

The each common-auxiliary electrode has connecting parts which connect to a common-auxiliary electrode in a pixel region neighboring the pixel region, and the connecting parts are formed on the side of the gate bus line 1.

A storage electrode 43 is formed overlapping the common-auxiliary electrode 15 and the gate bus line 1 (Fig. 9A), the common-auxiliary electrode 15, the gate bus line 1, and a common-auxiliary electrode in a pixel region neighboring the pixel region (Fig. 9B), the common-auxiliary electrode 15 and a common-auxiliary electrode in a pixel region neighboring the pixel region (Fig. 9C) to obtain storage capacitance.

In the multi-domain liquid crystal display device of the in the Fig. 10, two common-auxiliary electrode 15 surround the pixel region and are driven independently in the pixel region. The each common-auxiliary electrode has two connecting parts which connect to a common-auxiliary electrode in a pixel region neighboring the pixel region.

A storage electrode 43 is formed overlapping the common-auxiliary electrode 15 and the gate bus line 1 (Fig. 10A), the common-auxiliary electrode 15, the gate bus line 1, and a common-auxiliary electrode in a pixel region neighboring the pixel region (Fig. 10B), the common-auxiliary electrode 15 and a common-auxiliary electrode in a pixel region neighboring the pixel region (Fig. 10C) to obtain storage capacitance.

In the multi-domain liquid crystal display device of the in the Fig. 11, two common-auxiliary electrode 15 surround the pixel region. The each common-auxiliary electrode has connecting parts which connect to a common-auxiliary electrode in a pixel region neighboring the pixel region, and the common-auxiliary electrodes are connected each other at one of the connecting parts.

A storage electrode 43 is formed overlapping the common-auxiliary electrode 15 and the gate bus line 1 (Fig. 11A), the common-auxiliary electrode 15, the gate bus line 1, and a common-auxiliary electrode in a pixel region neighboring the pixel region (Fig. 11B), the common-auxiliary electrode 15 and a common-auxiliary electrode in a pixel region neighboring the pixel region (Fig. 11C) to obtain storage capacitance.

In the multi-domain liquid crystal display device of the

in the Fig. 12, two common-auxiliary electrode 15 surround the pixel region. The each common-auxiliary electrode has connecting parts which connect to a common-auxiliary electrode in a pixel region neighboring the pixel region, and the common-auxiliary electrodes are connected each other at the every connecting parts.

A storage electrode 43 is formed overlapping the common-auxiliary electrode 15 and the gate bus line 1 (Fig. 12A), the common-auxiliary electrode 15, the gate bus line 1, and a common-auxiliary electrode in a pixel region neighboring the pixel region (Fig. 12B), the common-auxiliary electrode 15 and a common-auxiliary electrode in a pixel region neighboring the pixel region (Fig. 12C) to obtain storage capacitance.

In the multi-domain liquid crystal display device of the in the Fig. 13, one common-auxiliary electrode 15 surrounds the pixel region. The common-auxiliary electrode has connecting parts which connect to a common-auxiliary electrode in a pixel region neighboring the pixel region.

A storage electrode 43 is formed overlapping the common-auxiliary electrode 15 and the gate bus line 1 (Fig. 13A), the common-auxiliary electrode 15, the gate bus line 1, and a common-auxiliary electrode in a pixel region neighboring the pixel region (Fig. 13B), the common-auxiliary electrode

15 and a common-auxiliary electrode in a pixel region neighboring the pixel region (Fig. 13C) to obtain storage capacitance.

Figs. 4A and 4C show that passivation layer 37 includes  $\text{SiN}_x$  or  $\text{SiO}_x$  and Figs. 4B and 4D show that passivation layer 37 includes BCB or acrylic resin in the LCD. Also, Figs. 4A and 4B show that dielectric frame 53 is formed on the common electrode 17, and Figs. 4C and 4D show that window inducing electric field 51 is formed in the common electrode 17.

Fig. 14 is a plan view of the multi-domain liquid crystal display devices according to the eighth embodiment of the present invention, Fig. 15 is a sectional view taken along the line VI-VI of Fig. 14, Fig. 16 is a plan view of the multi-domain liquid crystal display devices according to the ninth embodiment of the present invention, Fig. 17 is a plan view of the multi-domain liquid crystal display devices according to the tenth embodiment of the present invention, Fig. 18 is a plan view of the multi-domain liquid crystal display devices according to the eleventh embodiment of the present invention, Fig. 19 is a plan view of the multi-domain liquid crystal display devices according to the twelfth embodiment of the present invention, Fig. 20 is a plan view of the multi-domain liquid crystal display devices according to the thirteenth embodiment of the present invention.



Regarding to the multi-domain liquid crystal display devices according to the eighth to thirteenth embodiments of the present invention, the LCDs have the same structures of the first to sixth embodiments except not comprising the storage electrode. The pixel electrode 13 overlaps the common-auxiliary electrode 15 to obtain storage capacitance, and the light shielding layer 25 overlaps the common-auxiliary electrode 15. The Fig. 15 shows the structure that it does not form the storage electrode in LCDs.

Figs. 21A to 21C are plan views of the multi-domain liquid crystal display devices according to the fourteenth embodiment of the present invention, Fig. 22 is a sectional view taken along the line VII-VII of Fig. 21A, Figs. 23A to 24D are sectional views taken along the line VIII-VIII of Fig. 21A, Fig. 24 is a sectional view taken along the line IX-IX of Fig. 21B, Figs. 25A to 25C are plan views of the multi-domain liquid crystal display devices according to the fifteenth embodiment of the present invention, Figs. 26A to 26C are plan views of the multi-domain liquid crystal display devices according to the sixteenth embodiment of the present invention, Figs. 27A to 27C are plan views of the multi-domain liquid crystal display devices according to the seventeenth embodiment of the present invention, Figs. 28A to 28C are plan views of the multi-domain liquid crystal display

devices according to the eighteenth embodiment of the present invention, Figs. 29A to 29C are plan views of the multi-domain liquid crystal display devices according to the nineteenth embodiment of the present invention, Figs. 30A and 30B are plan views of the multi-domain liquid crystal display devices according to the twentieth embodiment of the present invention.

Regarding to the multi-domain liquid crystal display devices according to the fourteenth to twentieth embodiments of the present invention, the LCDs have the same structures of the first to sixth embodiments except that the pixel electrode 13 does not overlap the common-auxiliary electrode 15, and the light shielding layer 25 overlaps the pixel electrode 13.

Fig. 22 show the structure that a storage electrode 43 overlaps the common-auxiliary electrode 15 and the gate bus line 1 to obtain storage capacitance.

In the Fig. 24, a storage electrode 43 is formed overlapping the common-auxiliary electrode 15, the gate bus line 1, and a common-auxiliary electrode in a pixel region neighboring the pixel region, or the common-auxiliary electrode 15 and a common-auxiliary electrode in a pixel region neighboring the pixel region to obtain storage capacitance.

Figs. 23a and 23C show that passivation layer 37 includes  $\text{SiN}_x$  or  $\text{SiO}_x$  and Figs. 23B and 23D show that passivation layer 37 includes BCB or acrylic resin in the LCD.

Also, Figs. 23A and 23B show that dielectric frame 53 is formed on the common electrode 17, and Figs. 23C and 23D show that window inducing electric field 51 is formed in the common electrode 17.

Figs. 32A and 32B are plan views of the multi-domain liquid crystal display device according to the twenty-first embodiment of the present invention, Fig. 32C is a sectional view taken along the line I-I of Fig. 32A, and Figs. 32D to 32G are sectional view taken along the lines I-I, II-II of Fig. 32A.

In the twenty-first embodiment of the present invention, the pixel electrode 13 has a prolonged part toward the gate bus line 1 to partially overlap the storage electrode 43, the passivation 37 below the prolonged part is removed to form the contact hole 39 between the pixel electrode 13 and the storage electrode 43.

The pixel electrode 13 overlaps common-auxiliary electrode 15, the light shielding layer 25 overlaps the common-auxiliary electrode 15, and the storage electrode 43 overlaps common-auxiliary electrode 15 and gate bus line 1 to forms a storage capacitance.

Figs. 32D and 32E show that dielectric frame 53 is formed on the common electrode 17, and Figs. 32F, 32G show that electric field inducing window 51 is formed in the common electrode 17. Also, Figs. 32D and 32F show that passivation layer 37 includes  $\text{SiN}_x$  or  $\text{SiO}_x$  and Figs. 32E and 32G show that passivation layer 37 includes BCB or acrylic resin in the LCD.

Figs. 33A, 33B are plan views of the multi-domain liquid crystal display device according to the twenty-second embodiment of the present invention, Figs. 33C, 33D are sectional views taken along the line III-III of Fig. 33B, and Figs. 33E to 33H are sectional views taken along the line IV-IV of the Fig. 33A.

The pixel electrode 13 does not overlap common-auxiliary electrode 15, the light shielding layer 25 overlaps the common-auxiliary electrode 15, and the storage electrode 43 overlaps common-auxiliary electrode 15 and gate bus line 1 to forms a storage capacitance.

At this time, the gate insulator 35 and the passivation layer 37 on the common-auxiliary electrode 15 are removed, which intensifies the electric field of common-auxiliary electrode 15 that is applied to the pixel electrode 13.

The Fig. 33C shows a portion of the gate insulator 35

and passivation layer 37 that are removed to expose a part of the common-auxiliary electrode 15, and the Fig. 3D shows that three sides of the common-auxiliary electrode 15 are exposed.

Figs. 33E and 33F show that dielectric frame 53 is formed on the common electrode 17, and Figs. 33G, 33H show that electric field inducing window 51 is formed in the common electrode 17. Also, Figs. 33E and 33G show that passivation layer 37 includes  $\text{SiN}_x$  or  $\text{SiO}_x$  and Figs. 33F and 33H show that passivation layer 37 includes BCB or acrylic resin in the LCD.

Figs. 34A and 34B are plan view and sectional view taken along the line V-V of Fig. 34A of the multi-domain liquid crystal display device according to the twenty-third embodiment of the present invention.

In this embodiment, a pair of upper-lower pixel regions co-owns one common-auxiliary electrode 15, and the storage electrodes 43 in the upper-lower pixel regions are formed on the common-auxiliary electrode 15, so that the aperture ratio is largely improved.

The pixel electrode 13 overlaps common-auxiliary electrode 15, the light shielding layer 25 overlaps the common-auxiliary electrode 15, and the storage electrode 43 overlaps common-auxiliary electrode 15 and gate bus line 1

to forms a storage capacitance.

In addition, in this embodiment it is possible to form the dielectric frame 53 on the common electrode 17 or the electric field inducing window 51 in the common electrode 17.

Figs. 35A and 35B are plan views and sectional views taken along the line VI-VI of Fig. 35B of the multi-domain liquid crystal display device according to the twenty-fourth embodiment of the present invention.

The multi-domain LCD has the same structure as in the third embodiment of the present invention except as follows.

Pixel electrode 13 does not overlap common-auxiliary electrode 15, and the light shielding layer 25 overlaps the pixel electrode 13. At this time, a portion of the gate insulator 35 and passivation layer 37 on the common-auxiliary electrode 15 are removed, which intensifies the electric field of common-auxiliary electrode 15 that is applied to the pixel electrode 13 (refer to Fig. 35B).

Fig. 35C shows a portion of the gate insulator 35 and passivation layer 37 that are removed to expose a part of the common-auxiliary electrode 15, and the Fig. 35D shows that three sides of the common-auxiliary electrode 15 are exposed.

In the multi-domain LCD of the present invention, the aperture ratio is enhanced by an optimum structure design of

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a n-line thin film transistor (USP 5,694,185) so as to reduce power consumption, increase luminance, and lower reflection, thus improving contrast ratio. Aperture ratio is increased by forming the TFT above the gate line and providing a n-line TFT. The parasitic capacitor, occurring between the gate bus line and the drain electrode, can be reduced when a TFT having the same channel length as the symmetrical TFT structure is manufactured due to effect of channel length extension.

Figs. 36A to 36G are plan views showing the various windows inducing electric field 51 and dielectric frames 53 according to the embodiments of the present invention.

The multi-domain LCD of the present invention has a dielectric frame 53 on the pixel electrode and/or common electrode, or an window inducing electric field 51 like a hole or slit in the pixel electrode, passivation layer, gate insulator, color filter layer, and/or common electrode by patterning, thereby electric field distortion effect and multi-domain are obtained.

That is, from forming window inducing electric field 51 or dielectric frame 53, the multi-domain is obtained by dividing each pixel into four domains such as in a + , x , or double Y shape, or dividing each pixel horizontally, vertically, and/or diagonally, and differently

alignment-treating or forming alignment directions on each domain and on each substrate.

Furthermore, in multi-domain LCD of the present invention, an alignment layer(not shown in the figure) is formed over the whole first and/or second substrates. The alignment layer includes a material such as polyamide or polyimide based materials, PVA (polyvinylalcohol), polyamic acid or  $\text{SiO}_2$ . When rubbing is used to determine an alignment direction, it should be possible to apply any material suitable for the rubbing treatment.

Moreover, it is possible to form the alignment layer with a photosensitive material such as PVCN (polyvinylcinnamate), PSCN (polysiloxanecinnamate), and CelCN (cellulosecinnamate) based materials. Any material suitable for the photo-aligning treatment may be used. Irradiating light once on the alignment layer determines the alignment or pretilt direction and the pretilt angle. The light used in the photo-alignment is preferably a light in a range of ultraviolet light, and any of unpolarized light, linearly polarized light, and partially polarized light can be used.

In the rubbing or photo-alignment treatment, it is possible to apply one or both of the first and second substrates, and to apply different aligning-treatment on each substrate.



From the aligning-treatment, a multi-domain LCD is formed with at least two domains, and LC molecules of the LC layer are aligned differently one another on each domain. That is, the multi-domain is obtained by dividing each pixel into four domains such as in a + or x shape, or dividing each pixel horizontally, vertically, and/or diagonally, and differently alignment-treating or forming alignment directions on each domain and on each substrate.

It is possible to have at least one domain of the divided domains unaligned. It is also possible to have all domains unaligned.

Consequently, since the multi-domain LCD of the present invention forms the gate electrode and the common-auxiliary electrode on the same layer and, a storage electrode 43 overlapping the common-auxiliary electrode 15, the gate bus line 1, and/or a common-auxiliary electrode in a pixel region neighboring the pixel region, an appropriate capacitance is obtained, the viewing angle is improved, and a high voltage is not needed to raise the intensity of the electric field applied between the two electrodes. Also, in the case of conducting an alignment-treatment, a high response time and a stable LC structure can be obtained by a pretilt angle and an anchoring energy. Moreover, the disclination is thus removed to thereby improve the brightness.

PATENT

It will be apparent to those skilled in the art that various modifications can be made in the liquid crystal display device of the present invention without departing from the spirit or scope of the invention. Thus, it is intended that the present invention covers the modifications and variations of this invention provided they come within the scope of the appended claims and their equivalents.

What Is Claimed Is:

1. A multi-domain liquid crystal display device comprising:  
first and second substrates facing each other;

a liquid crystal layer between said first and second substrates;

a plurality of gate bus lines arranged in a first direction on said first substrate and a plurality of data bus lines arranged in a second direction on said first substrate to define a plurality of pixel region;

a thin film transistor positioned at a crossing area of said data bus line and said gate bus line, said thin film transistor comprising a gate electrode, a semiconductor layer, and source/drain electrodes;

a common-auxiliary electrode comprising at least one electrode in the each pixel region;

a plurality of pixel electrodes electrically charged through the thin film transistor; and

an alignment layer on at least one substrate between said first and second substrates.

2. The multi-domain liquid crystal display device according to claim 1, wherein the common-auxiliary electrode has first

connecting parts which connect to a common-auxiliary electrode in a pixel region neighboring the pixel region in the first direction.

3. The multi-domain liquid crystal display device according to claim 2, wherein the first connecting parts have second connecting parts which connect to a common-auxiliary electrode in a pixel region neighboring the pixel region in the second direction.

4. The multi-domain liquid crystal display device according to claim 2, wherein the common-auxiliary electrode has protrusions crossing the first connecting parts.

5. The multi-domain liquid crystal display device according to claim 1, wherein said pixel electrode overlaps said common-auxiliary electrode.

6. The multi-domain liquid crystal display device according to claim 1, wherein said pixel electrode does not overlap said common-auxiliary electrode.

7. The multi-domain liquid crystal display device according to claim 1, further comprising;

a storage electrode overlapping said gate bus line.

8. The multi-domain liquid crystal display device according to claim 1, further comprising;

a storage electrode overlapping said common-auxiliary electrode.

9. The multi-domain liquid crystal display device according to claim 1, further comprising;

a storage electrode overlapping a common-auxiliary electrode in a pixel region neighboring the pixel region in the second direction.

10. The multi-domain liquid crystal display device according to claim 1, wherein said pixel region is divided into at least two portions, liquid crystal molecules in said liquid crystal layer in each portion being driven differently from each other.

11. The multi-domain liquid crystal display device according to claim 1, wherein said alignment layer is divided into at least two portions, liquid crystal molecules in said liquid crystal layer in each portion being aligned differently from

each other.

12. A multi-domain liquid crystal display device comprising:  
first and second substrates facing each other;

a liquid crystal layer between said first and second substrates;

a plurality of gate bus lines arranged in a first direction on said first substrate and a plurality of data bus lines arranged in a second direction on said first substrate to define a plurality of pixel region;

a common-auxiliary electrode surrounding said pixel region on a same layer whereon said gate bus line is formed;

a gate insulator over said whole first substrate;

a storage electrode overlapping said gate bus line on the gate insulator, the storage electrode connecting electrically the pixel electrode on an area except the pixel region;

a passivation layer on said gate insulator over said whole first substrate;

a pixel electrode electrically charged through said data bus line in said pixel region;

a light shielding layer on said second substrate;

a color filter layer on said light shielding layer;

a common electrode on said color filter layer; and

an alignment layer on at least one substrate between said first and second substrates.

13. The multi-domain liquid crystal display device according to claim 12, further comprising;

an n-line thin film transistor positioned at a crossing area of said data bus line and said gate bus line

14. The multi-domain liquid crystal display device according to claim 12, further comprising;

a storage electrode connecting said pixel electrode below said passivation layer and overlapping said common-auxiliary electrode.

15. The multi-domain liquid crystal display device according to claim 12, wherein said pixel electrode overlaps said common-auxiliary electrode.

16. The multi-domain liquid crystal display device according to claim 15, wherein said light shielding layer overlaps said common-auxiliary electrode.

17. The multi-domain liquid crystal display device according

to claim 12, wherein said pixel electrode does not overlap said common-auxiliary electrode.

18. The multi-domain liquid crystal display device according to claim 17, wherein said light shielding layer overlaps said pixel electrode.

19. The multi-domain liquid crystal display device according to claim 12, wherein said gate insulator and said passivation layer are formed in an area except said common-auxiliary electrode.

20. The multi-domain liquid crystal display device according to claim 12, wherein said common-auxiliary electrode is electrically connected to said common electrode.

21. The multi-domain liquid crystal display device according to claim 12, further comprising;  
a dielectric frame for distorting electric field on said pixel electrode.

22. The multi-domain liquid crystal display device according to claim 12, further comprising;  
a dielectric frame for distorting electric field on said

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common electrode.

23. The multi-domain liquid crystal display device according to claim 12, wherein said pixel electrode has a window inducing electric field therein.

24. The multi-domain liquid crystal display device according to claim 12, wherein said passivation layer has a window inducing electric field therein.

25. The multi-domain liquid crystal display device according to claim 12, wherein said gate insulator has a window inducing electric field therein.

26. The multi-domain liquid crystal display device according to claim 12, wherein said common electrode has a window inducing electric field therein.

27. The multi-domain liquid crystal display device according to claim 12, wherein said color filter layer has a window inducing electric field therein.

28. The multi-domain liquid crystal display device according to claim 12, further comprising;  
an over coat layer on said color filter layer.

29. The multi-domain liquid crystal display device according to claim 28, wherein said over coat layer has a window inducing electric field therein.

30. The multi-domain liquid crystal display device according to claim 12, wherein said passivation layer includes a material selected from the group consisting of BCB (BenzoCycloButene), acrylic resin, and polyimide compound.

31. The multi-domain liquid crystal display device according to claim 12, wherein said passivation layer includes a material selected from the group consisting of silicon nitride and silicon oxide.

32. The multi-domain liquid crystal display device according to claim 12, wherein said common-auxiliary electrode includes a material selected from the group consisting of ITO (indium tin oxide), aluminum, molybdenum, chromium, tantalum, titanium, and an alloy thereof.

33. The multi-domain liquid crystal display device according to claim 12, wherein said pixel electrode includes a material selected from the group consisting of ITO (indium tin oxide), aluminum, and chromium.

34. The multi-domain liquid crystal display device according to claim 12, wherein said common electrode includes ITO (indium tin oxide).

35. The multi-domain liquid crystal display device according to claim 12, wherein said pixel region is divided into at least two portions, liquid crystal molecules in said liquid crystal layer in each portion being driven differently from each other.

36. The multi-domain liquid crystal display device according to claim 12, wherein said alignment layer is divided into at least two portions, liquid crystal molecules in said liquid crystal layer in each portion being aligned differently from each other.

37. The multi-domain liquid crystal display device according

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to claim 36, wherein at least one portion of said at least two portions of the alignment layer is alignment-treated.

38. The multi-domain liquid crystal display device according to claim 36, wherein all portions of said at least two portions of the alignment layer are non-alignment-treated.

39. The multi-domain liquid crystal display device according to claim 12, wherein said liquid crystal layer includes liquid crystal molecules having positive dielectric anisotropy.

40. The multi-domain liquid crystal display device according to claim 12, wherein said liquid crystal layer includes liquid crystal molecules having negative dielectric anisotropy.

41. The multi-domain liquid crystal display device according to claim 12, further comprising:  
a negative uniaxial film on at least one substrate.

42. The multi-domain liquid crystal display device according to claim 12, further comprising:

a negative biaxial film on at least one substrate.

43. The multi-domain liquid crystal display device according to claim 12, wherein said liquid crystal layer includes chiral dopants.

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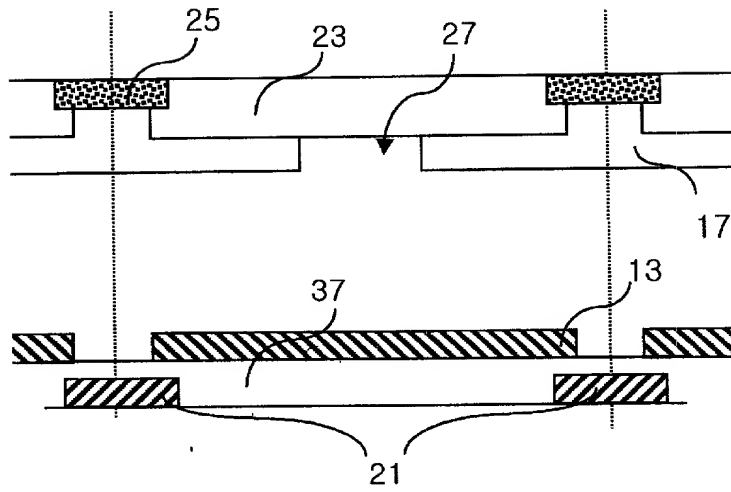
**ABSTRACT OF THE DISCLOSURE**

A multi-domain liquid crystal display device comprises first and second substrates facing each other; a liquid crystal layer between the first and second substrates; a plurality of gate bus lines arranged in a first direction on the first substrate and a plurality of data bus lines arranged in a second direction on the first substrate to define a plurality of pixel region; a thin film transistor positioned at a crossing area of the data bus line and the gate bus line, the thin film transistor comprising a gate electrode, a semiconductor layer, and source/drain electrodes; a common-auxiliary electrode comprising at least one electrode in the each pixel region; a plurality of pixel electrodes electrically charged through the thin film transistor; and an alignment layer on at least one substrate between the first and second substrates.

DRAWINGS

FIG. 1

PRIOR ART



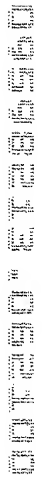




FIG. 2B

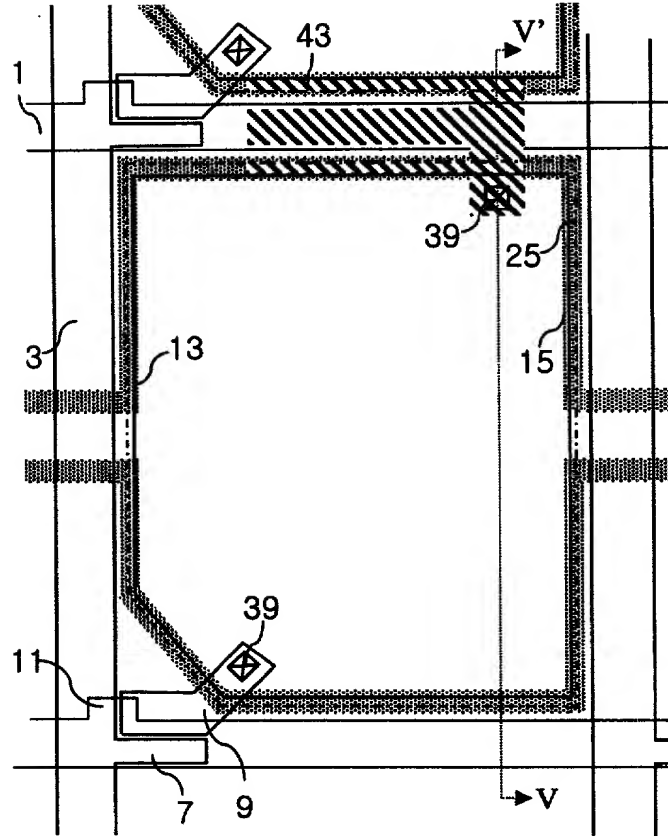


FIG. 2C

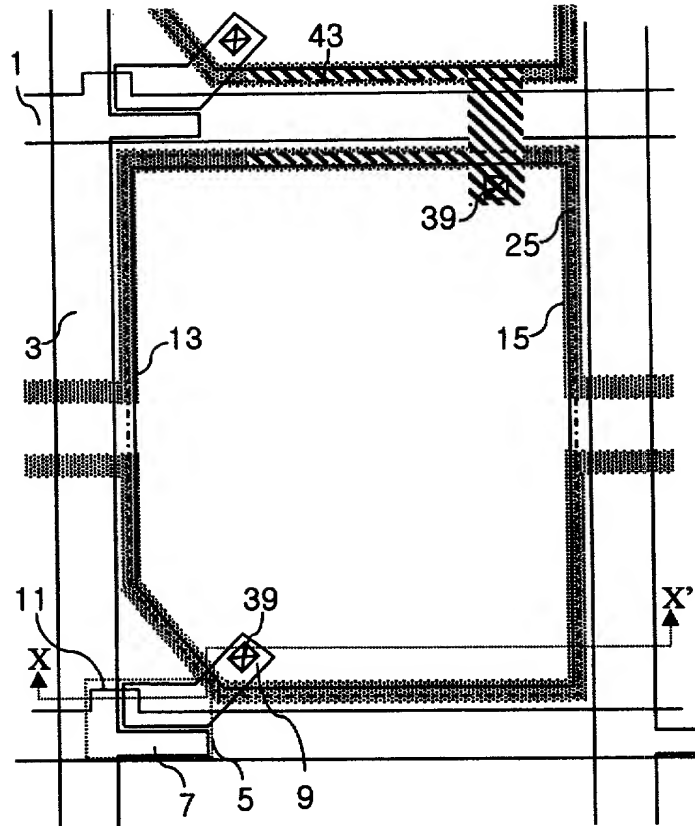


FIG. 3

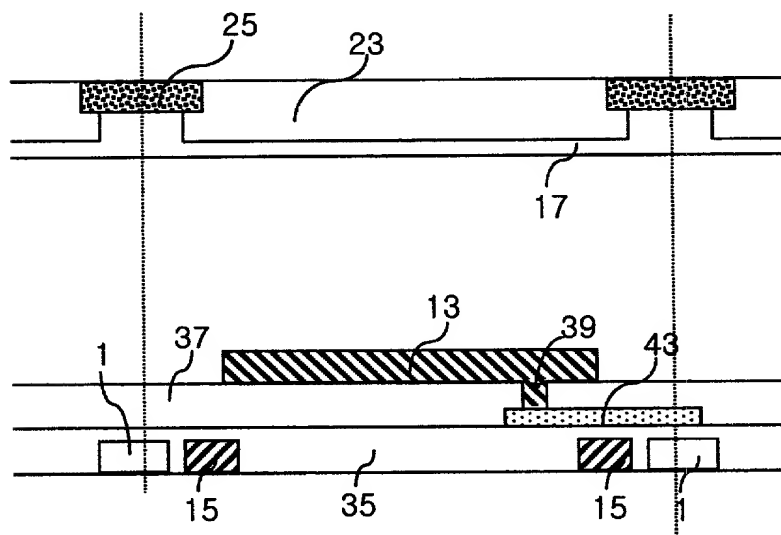




FIG. 4C

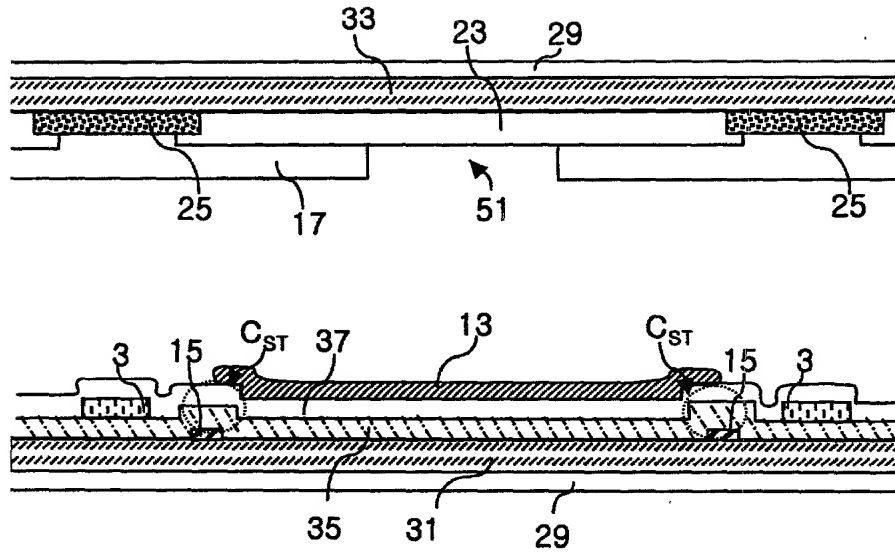


FIG. 4D

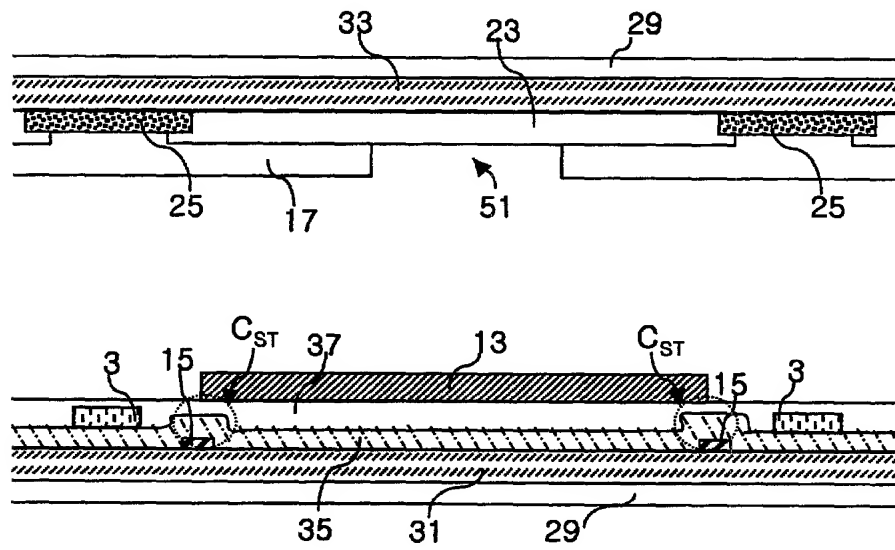


FIG. 5

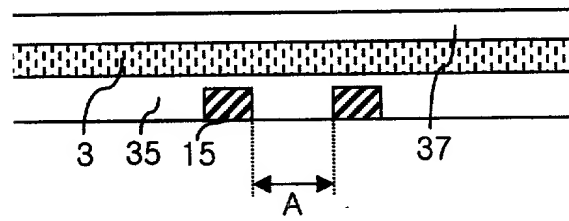
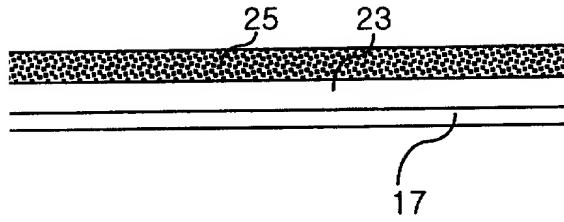


FIG. 6

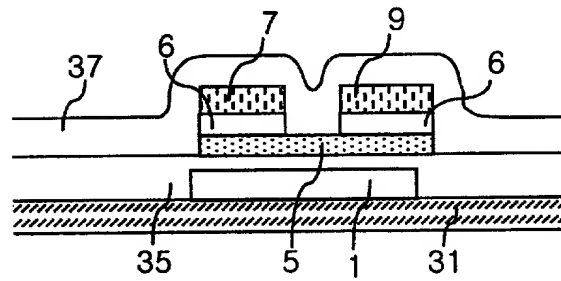


FIG. 7

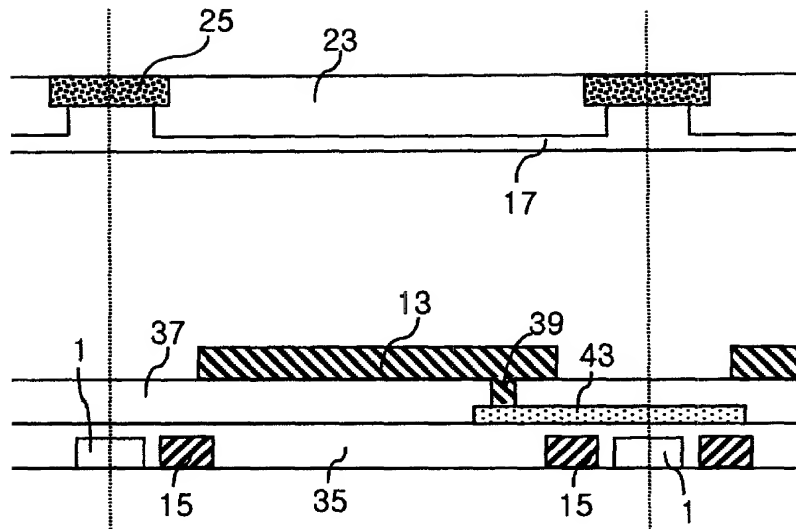
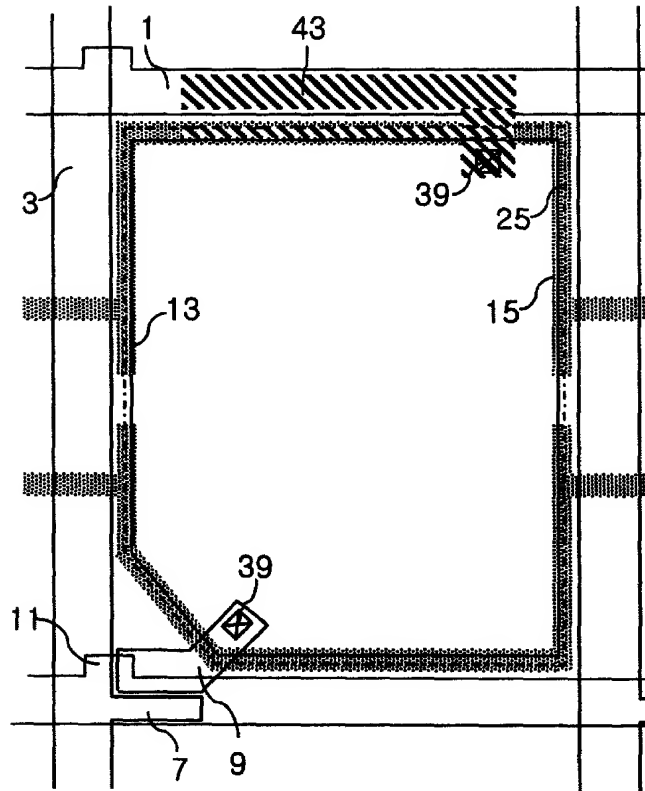
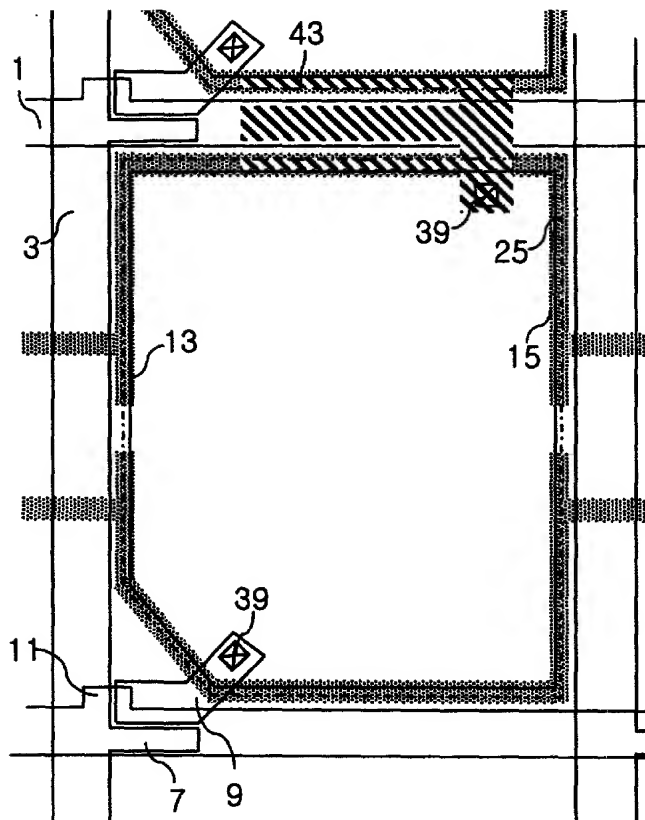


FIG. 8A



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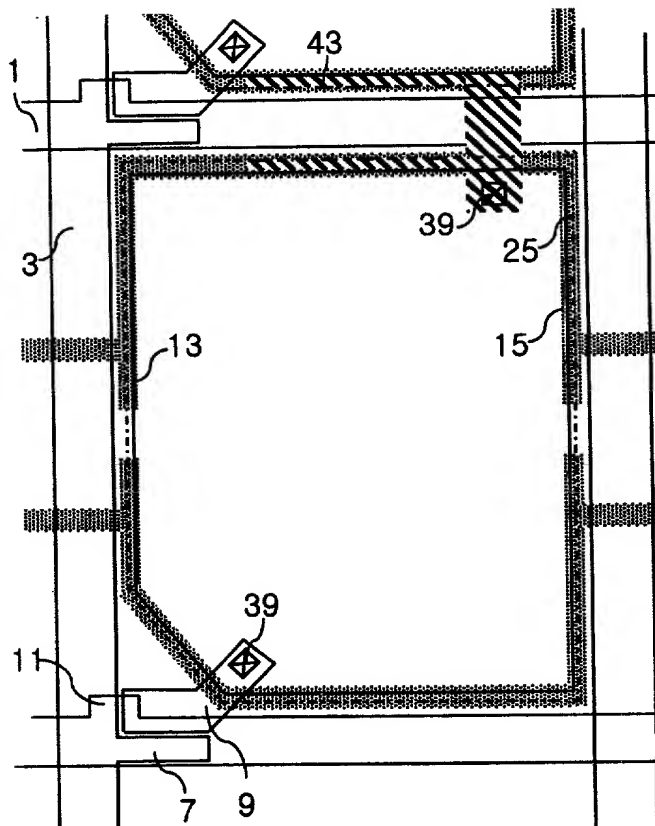


FIG. 9A

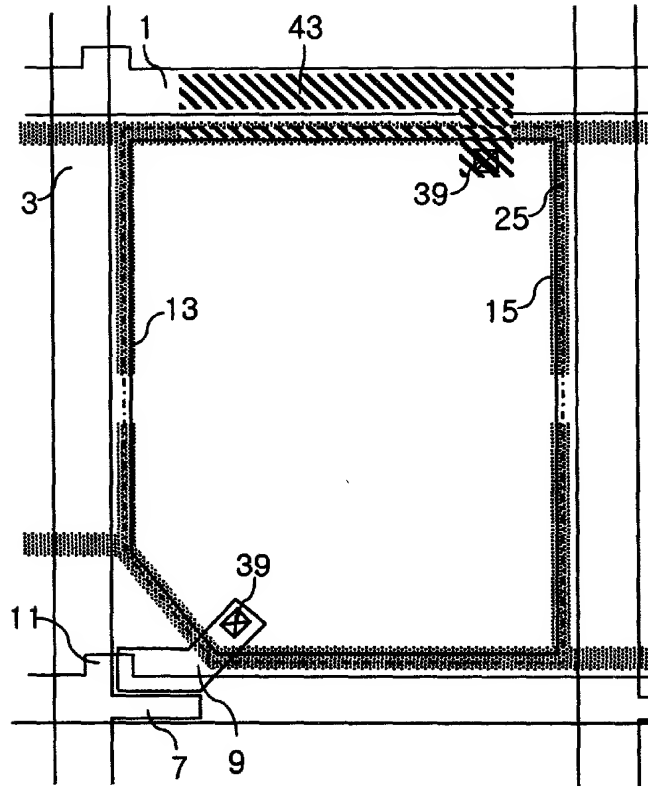


FIG. 9B

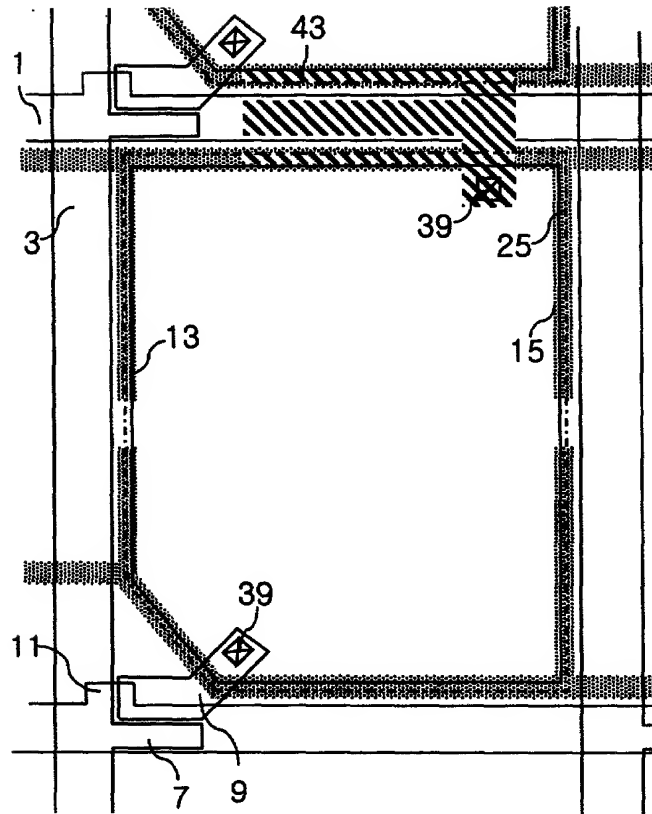
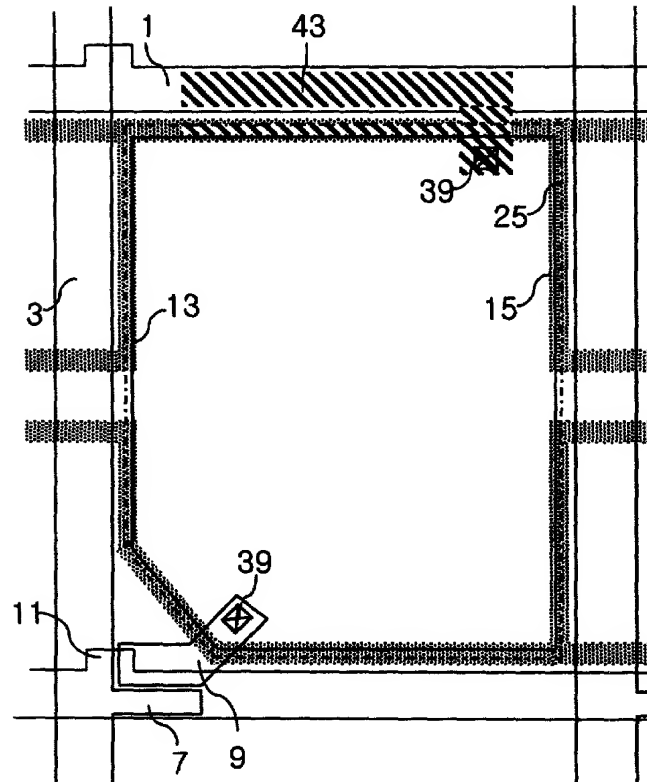


FIG. 1 is a schematic cross-sectional view of a semiconductor device. The device features a central rectangular region 13, which is surrounded by a thick frame 15. The frame 15 consists of vertical sections 25 and horizontal sections 39. At the top-left corner of the frame, there is a diamond-shaped feature 43. At the bottom-left corner, there is another diamond-shaped feature 39. Other labeled components include 1, 3, 7, 9, and 11, which represent various layers or structures surrounding the central region.

FIG. 10A



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FIG. 10B

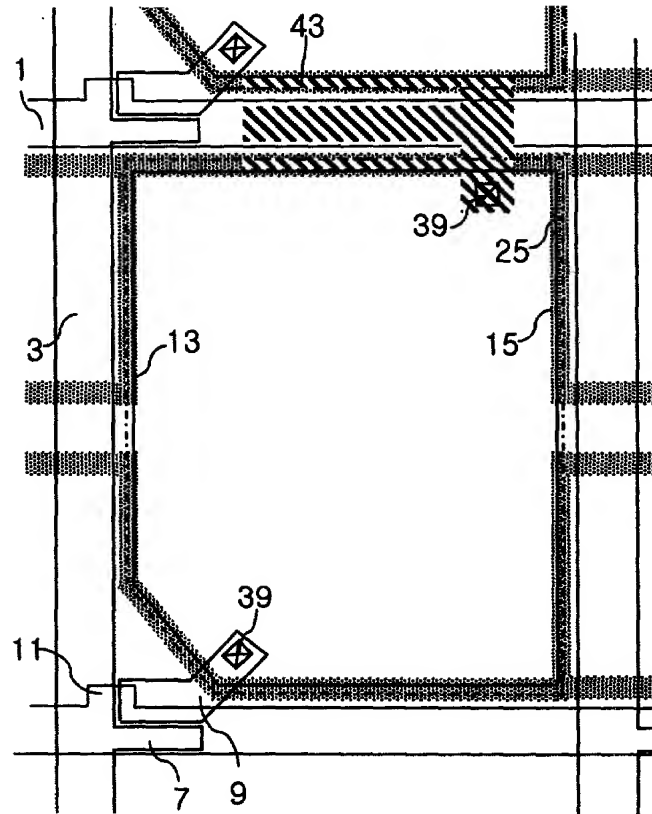


FIG. 10C

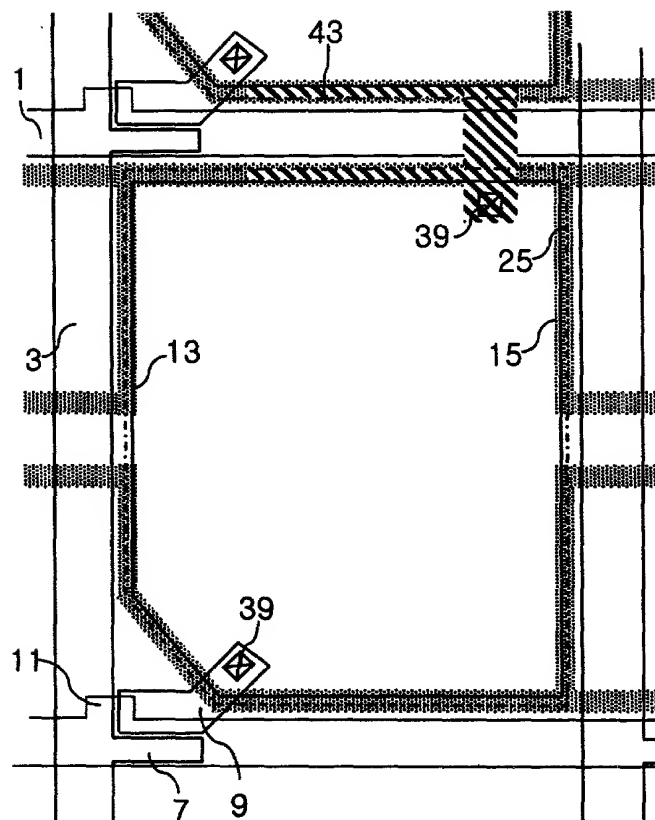


FIG. 11A

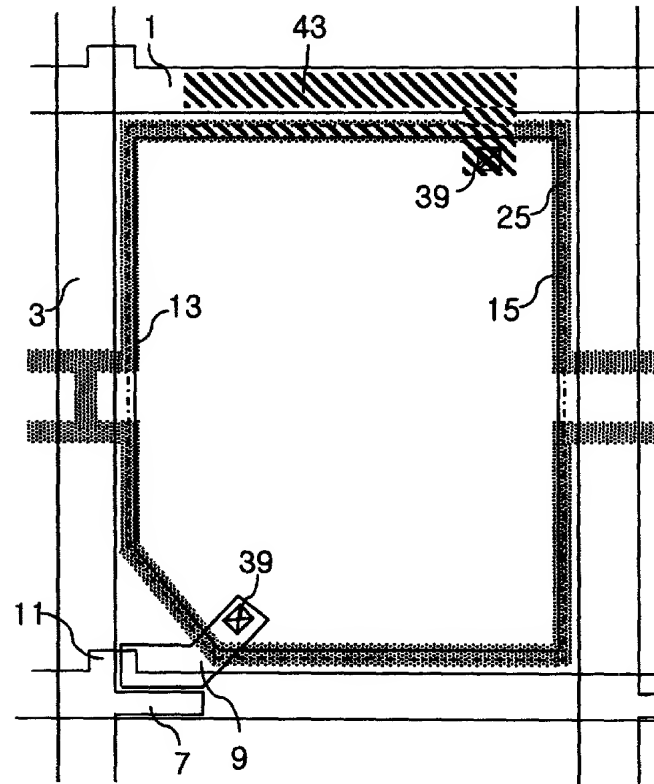




FIG. 11B

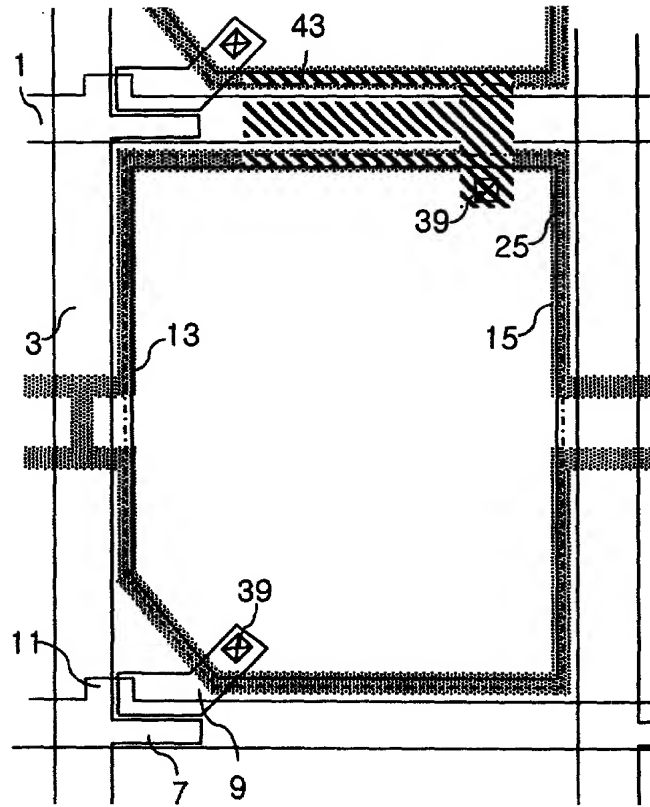


FIG. 11C

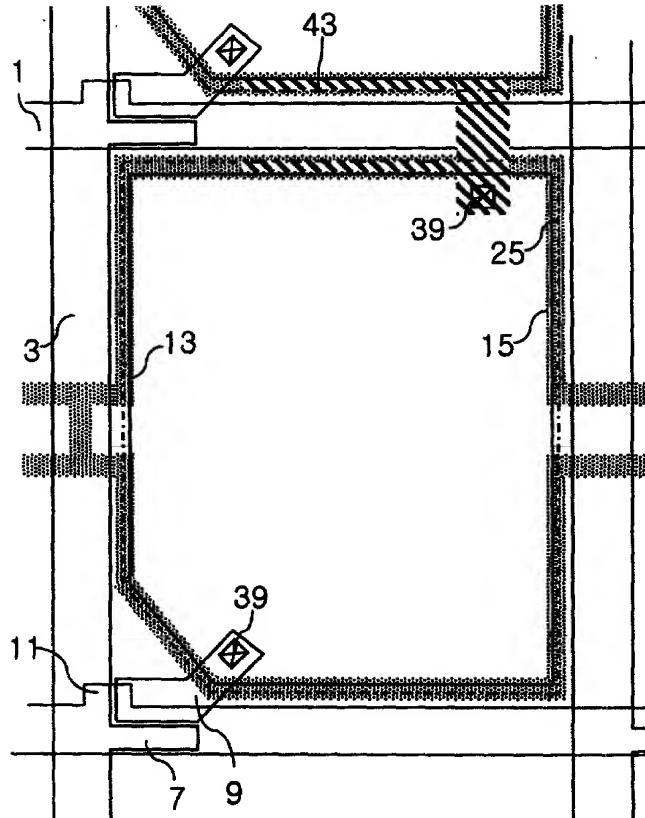


FIG. 12A

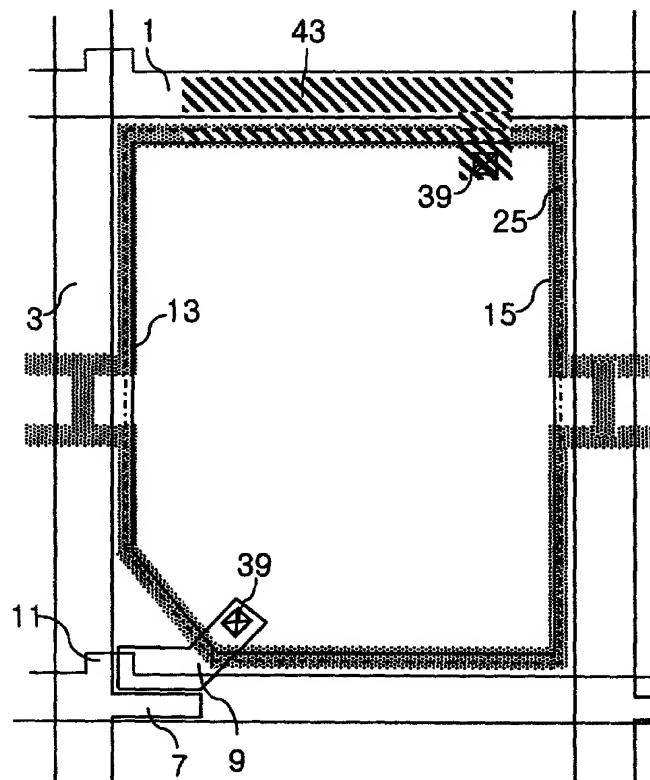


FIG. 12B

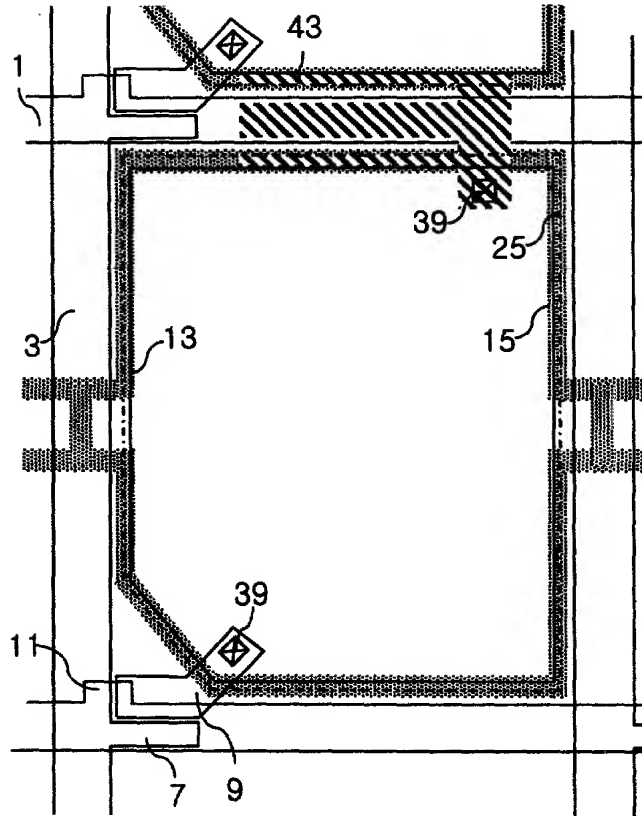


FIG. 12C

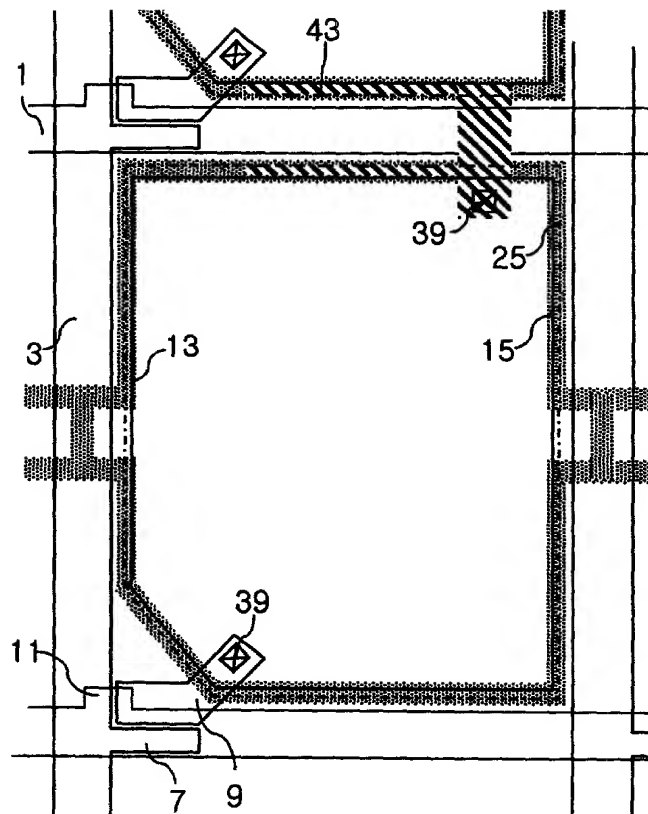


FIG. 13A

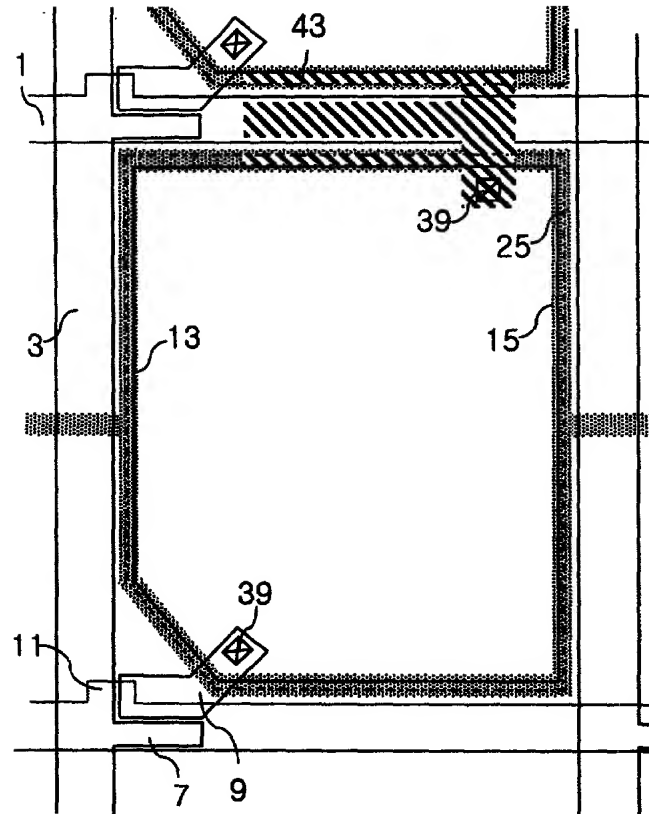


FIG. 13B

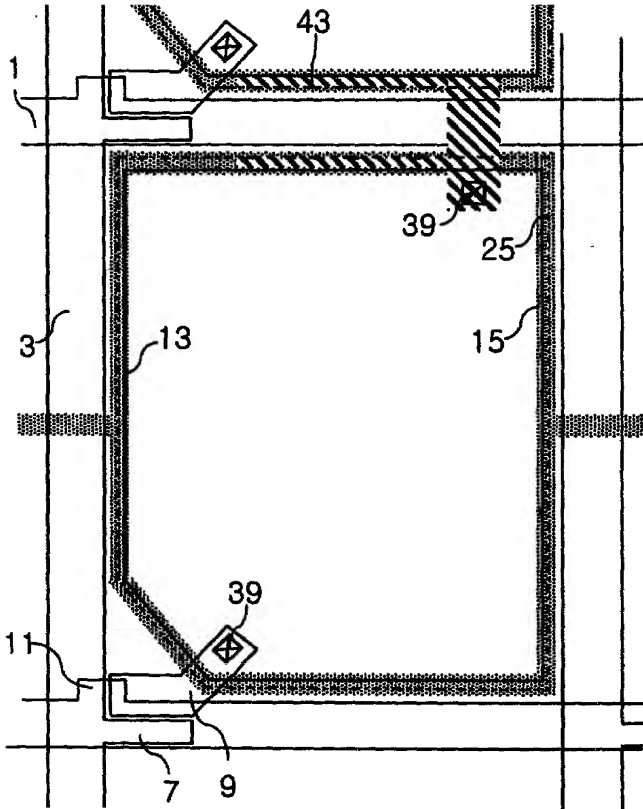


FIG. 14

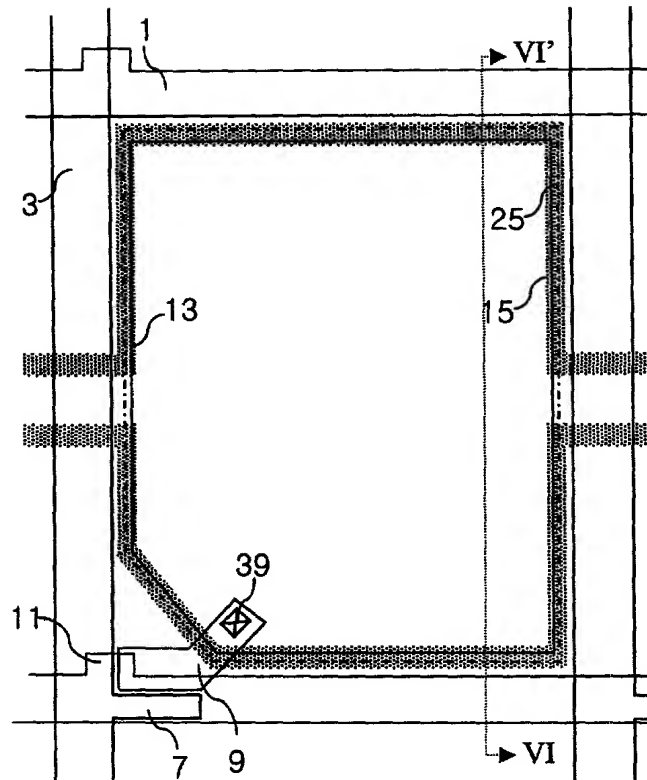


FIG. 15

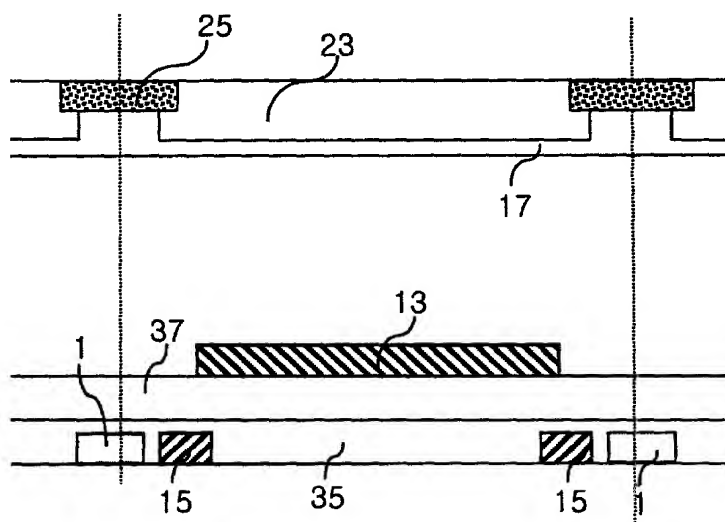




FIG. 16

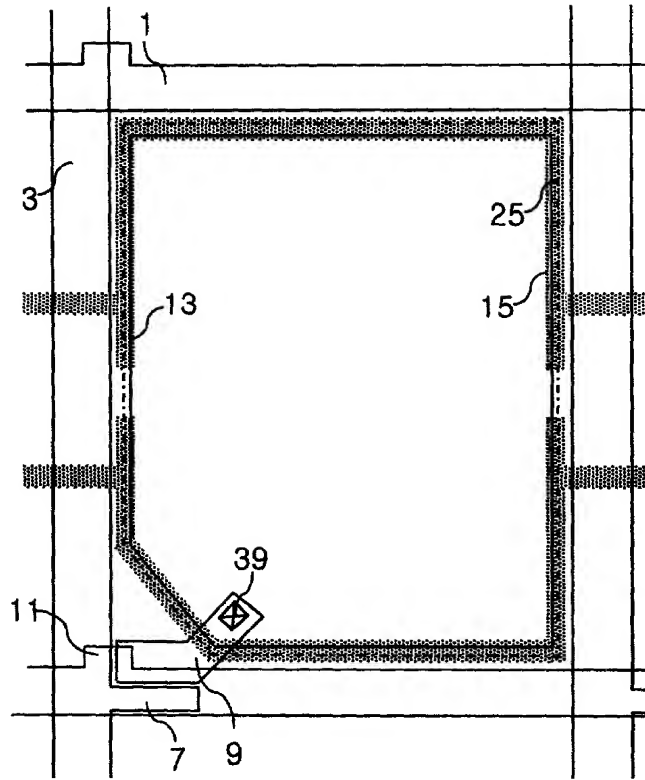


FIG. 17

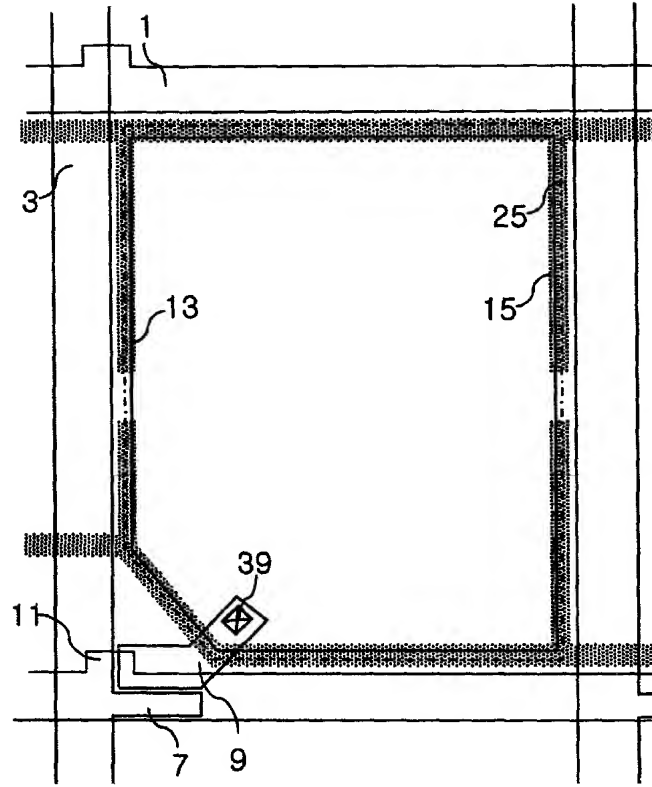




FIG. 19

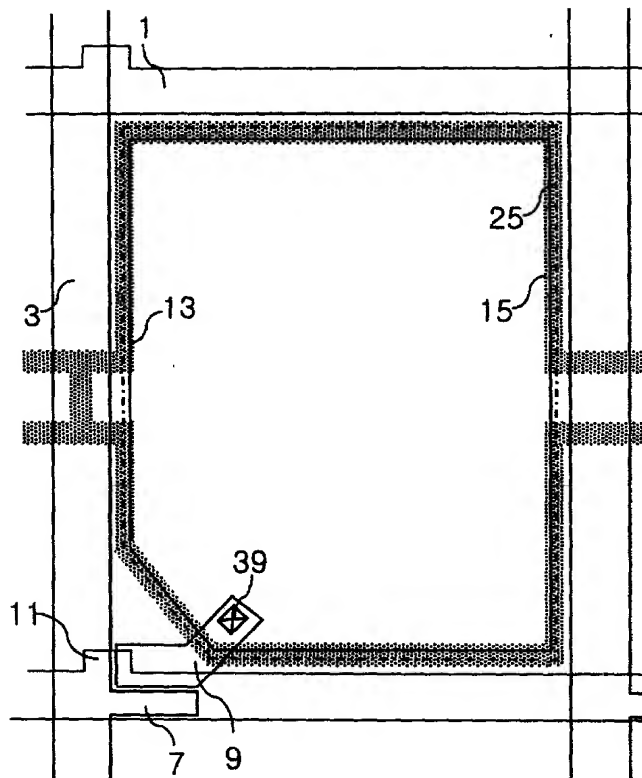


FIG. 20

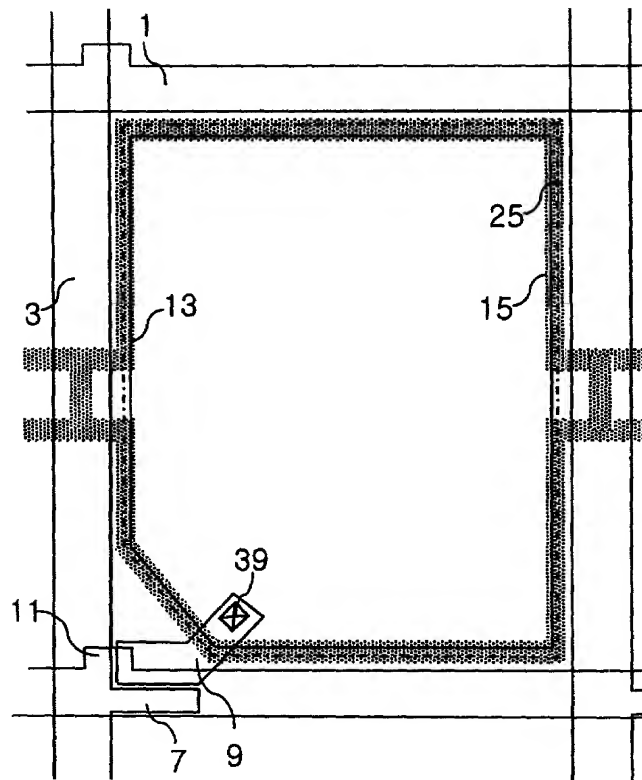


FIG. 21A

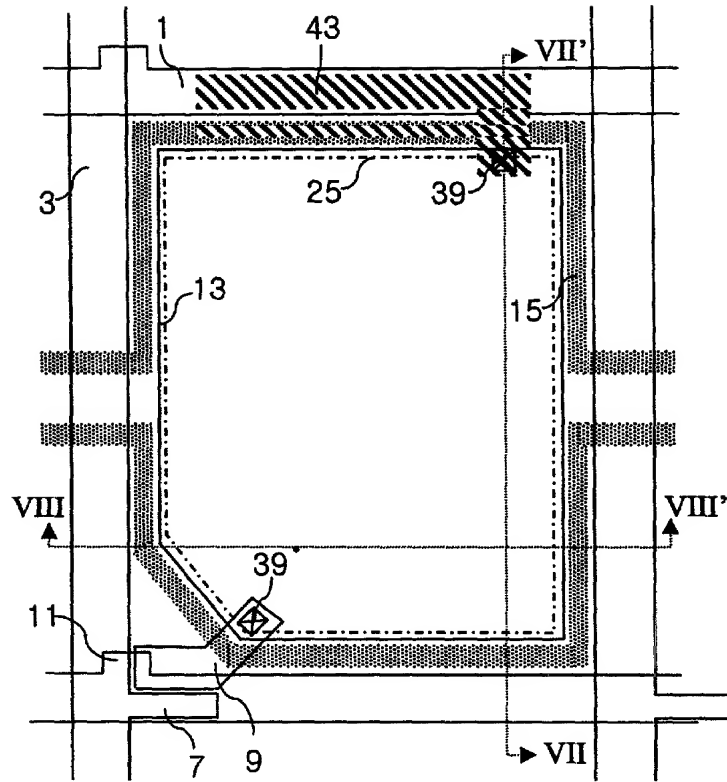


FIG. 21B

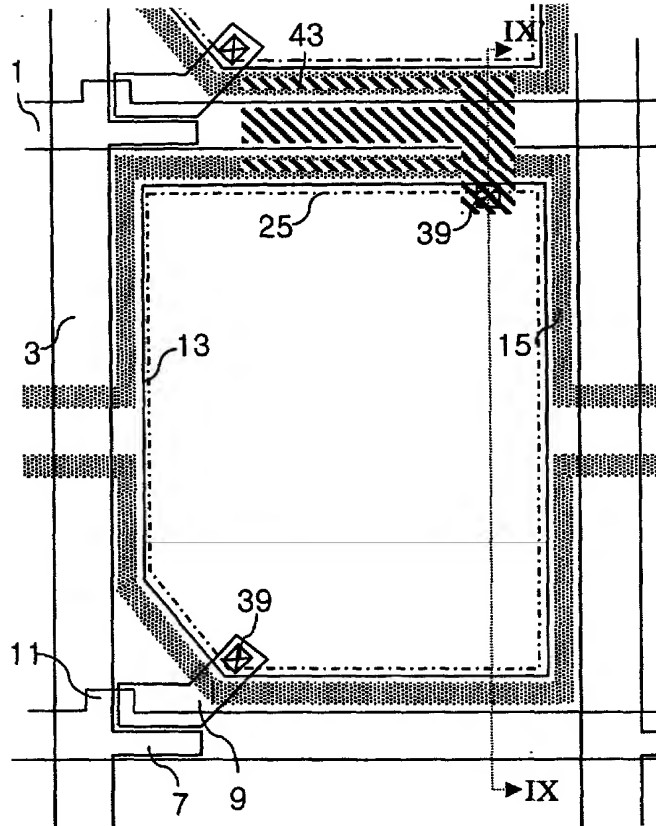


FIG. 21C

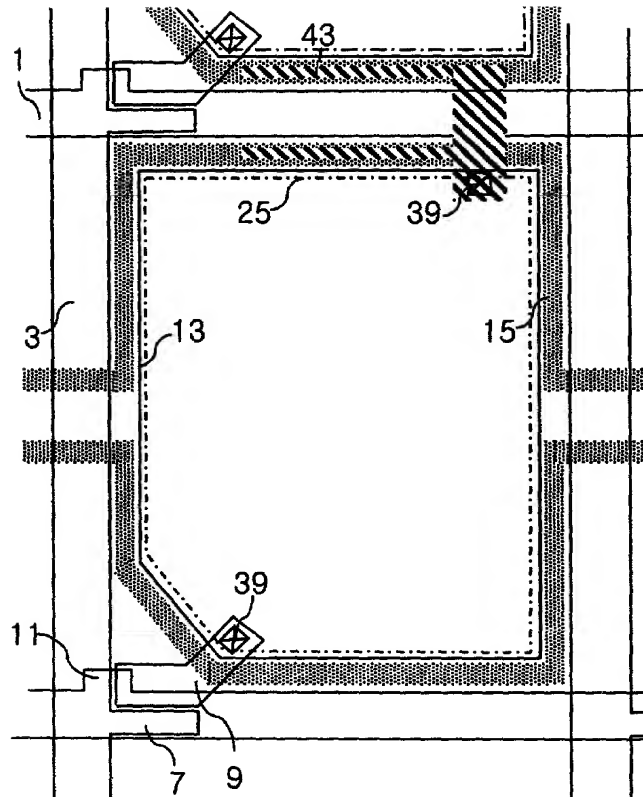


FIG. 22

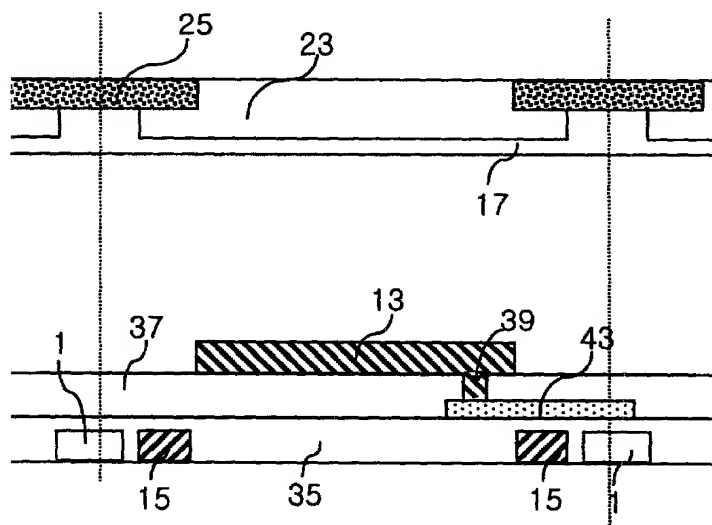




FIG. 23A

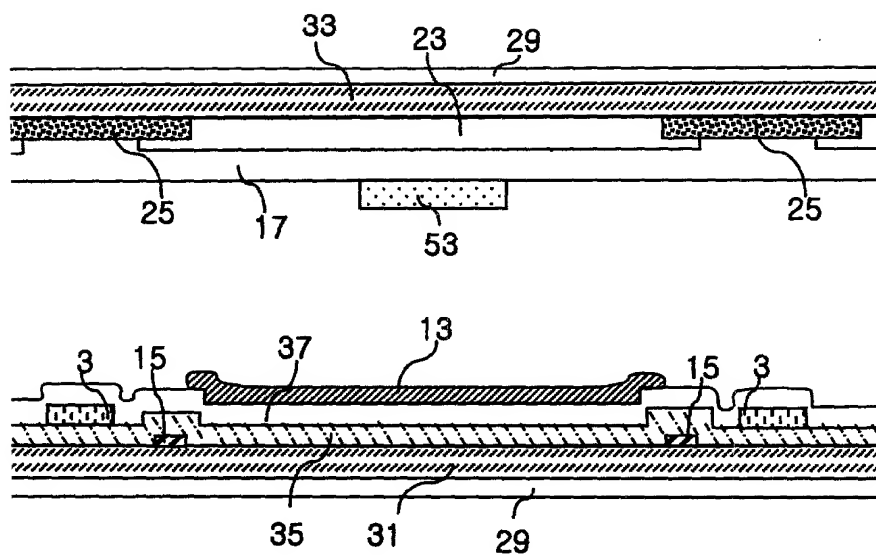


FIG. 23B

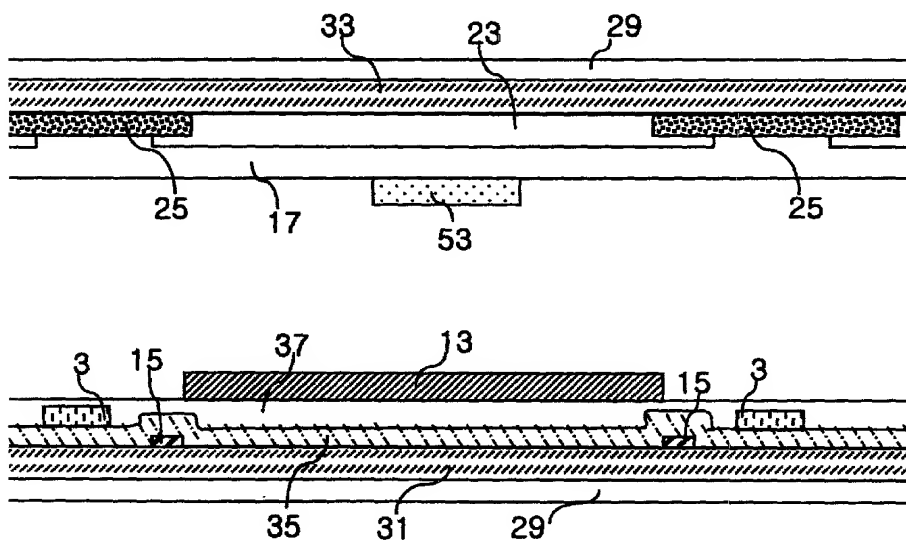


FIG. 23C

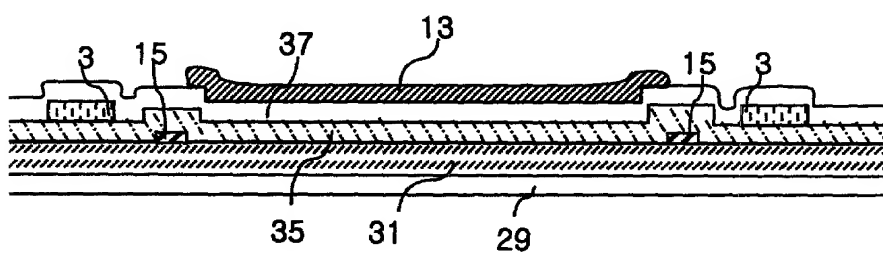
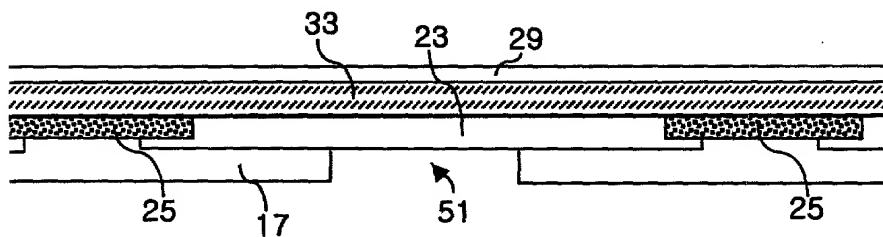


FIG. 23D

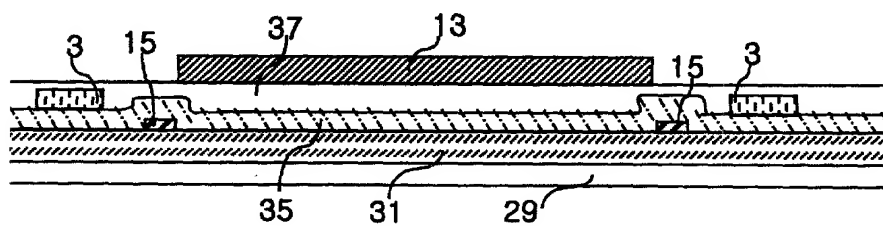
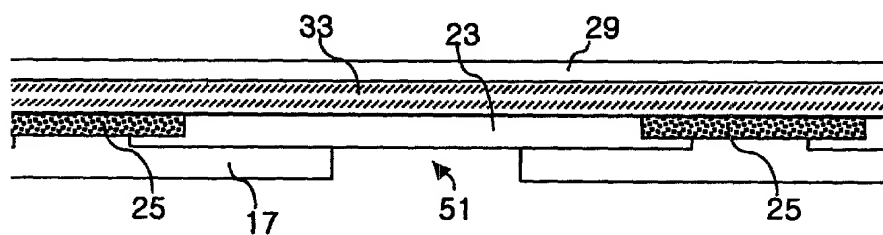


FIG. 24

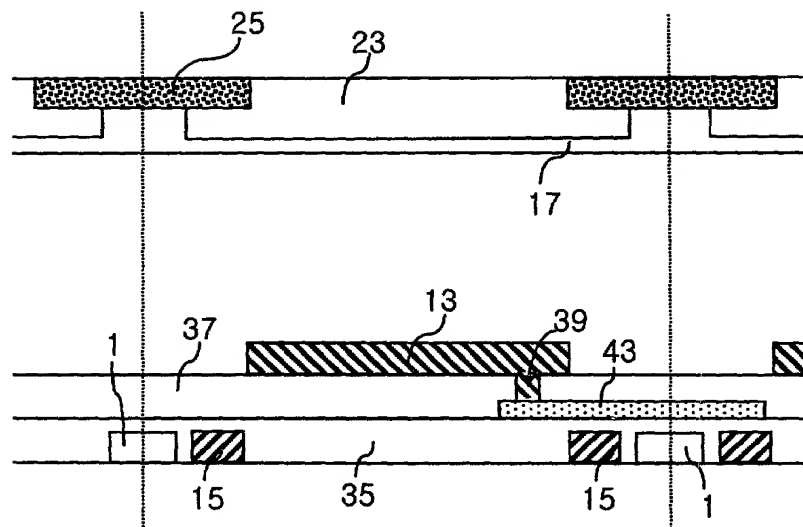


FIG. 25A

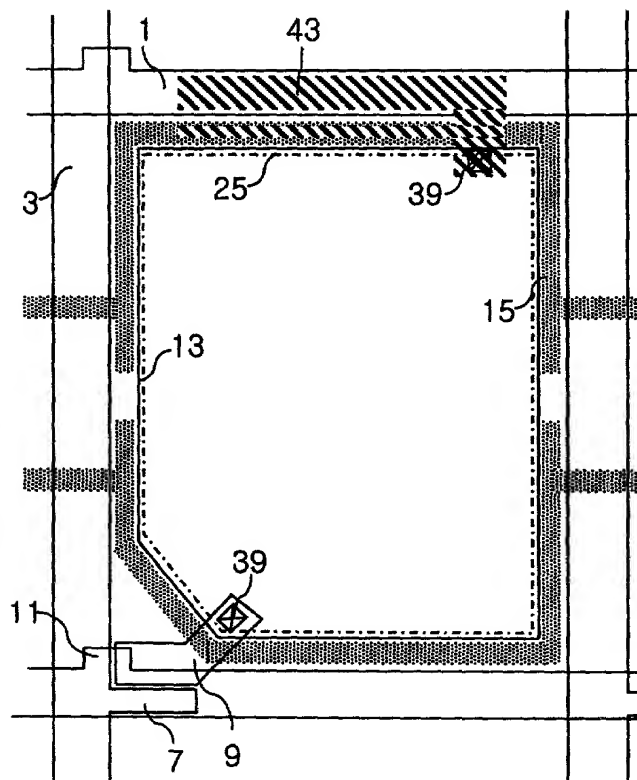


FIG. 25B

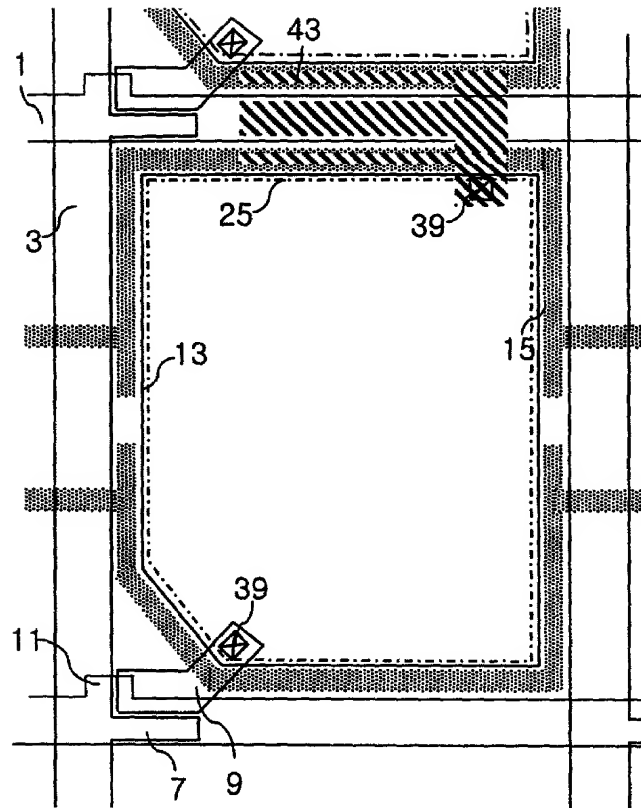


FIG. 25C

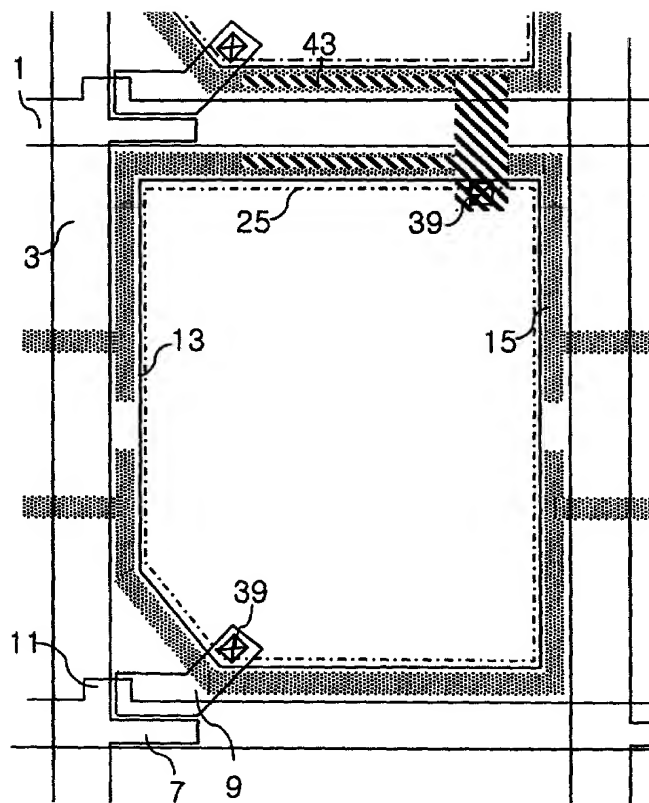




FIG. 26B

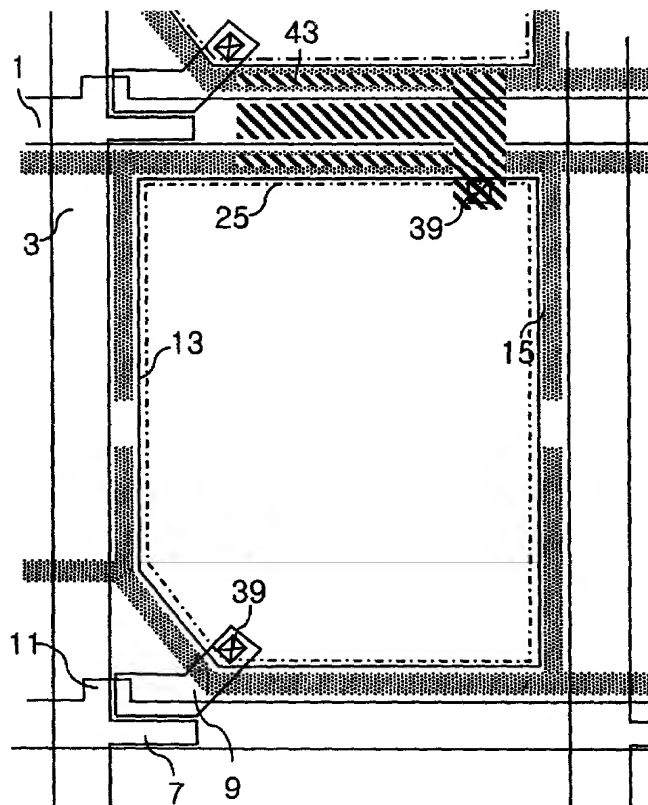




FIG. 26C

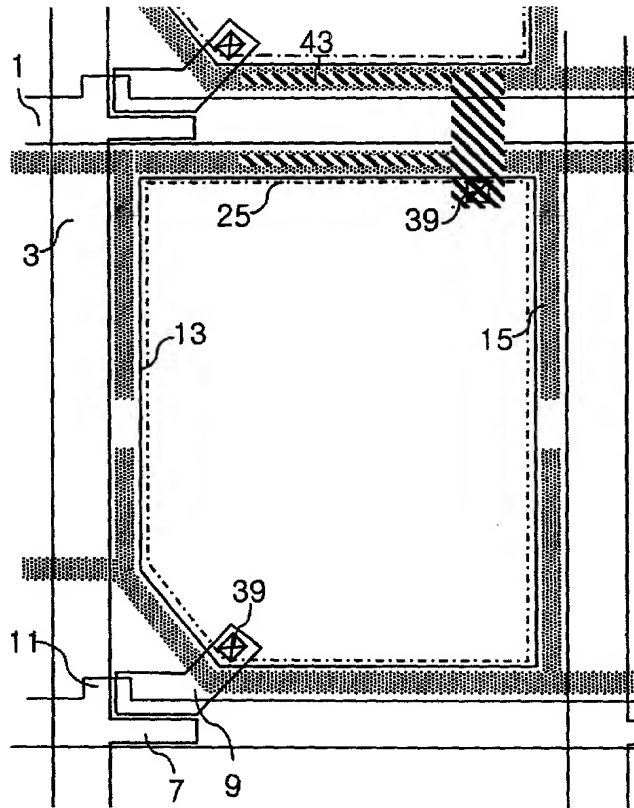


FIG. 27A

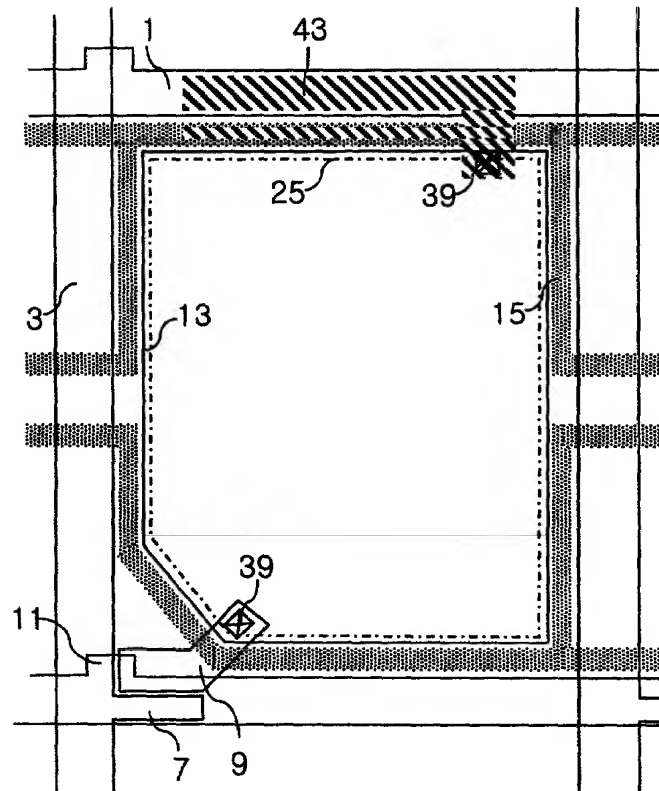


FIG. 27B

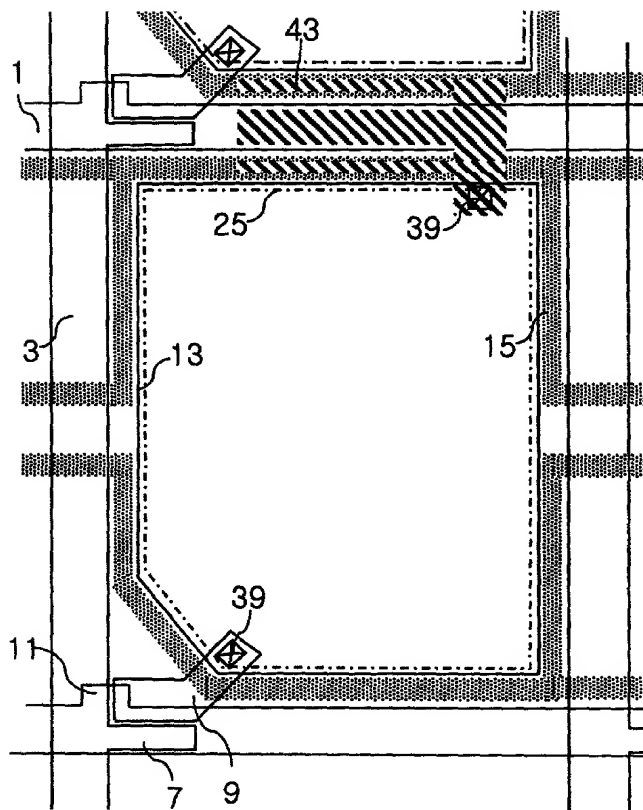


FIG. 27C

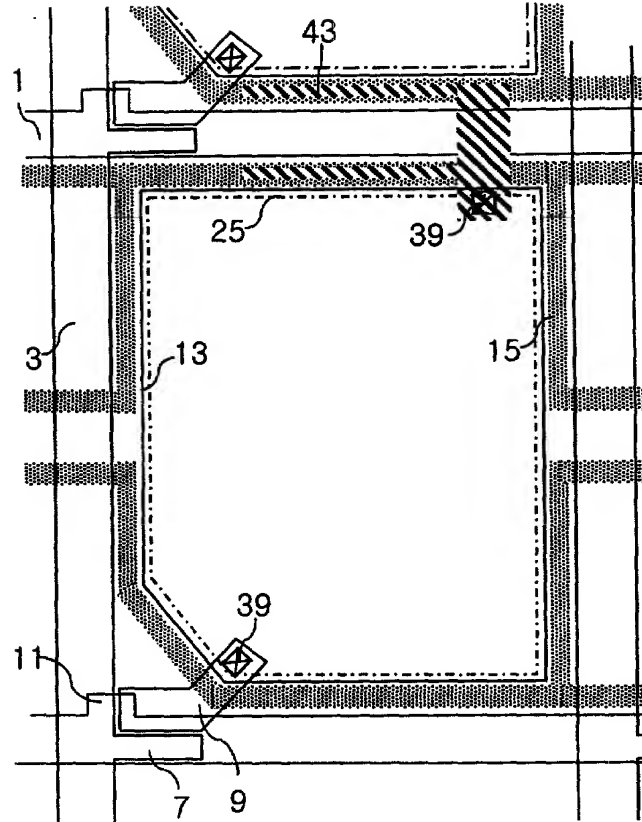


FIG. 28A

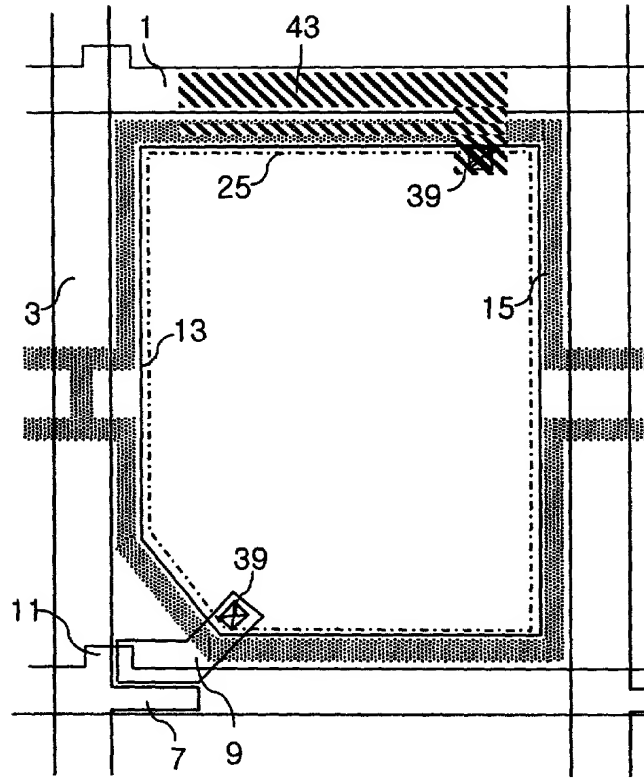


FIG. 28B

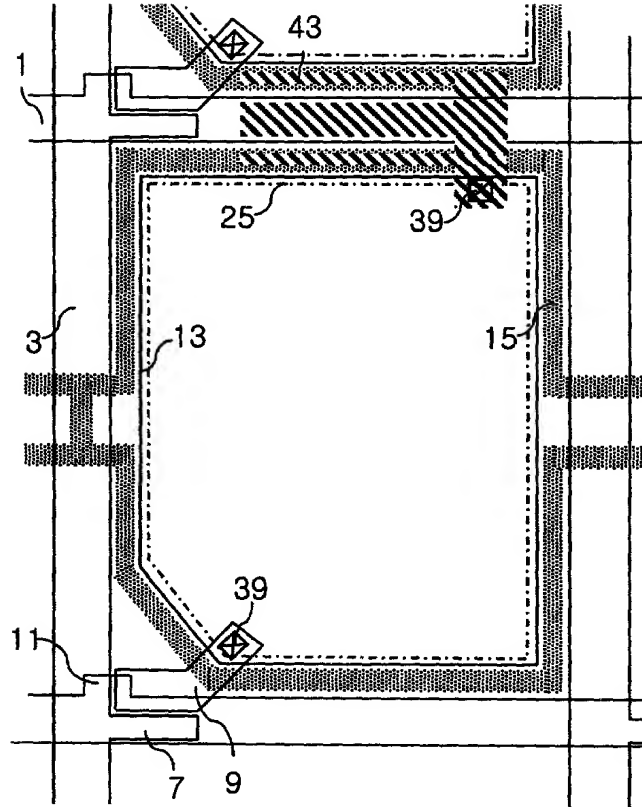


FIG. 28C

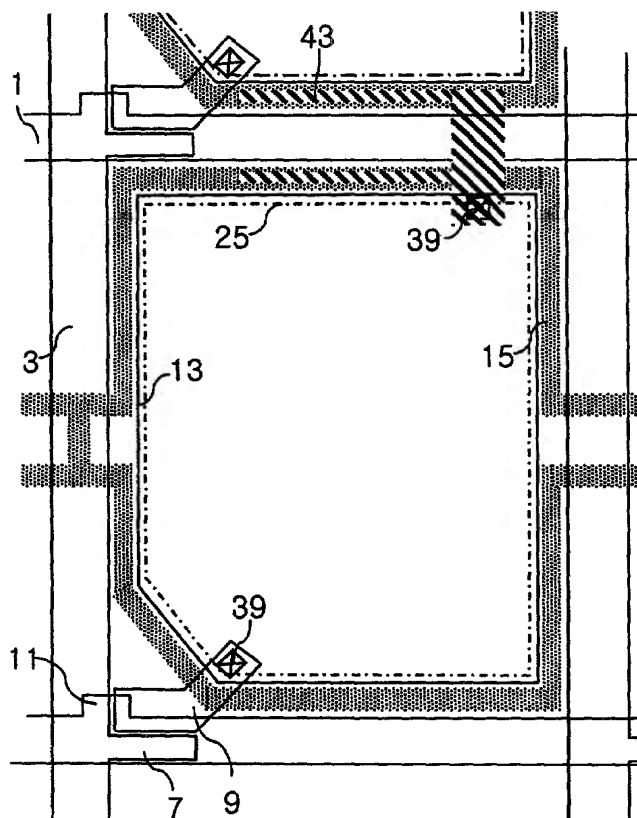


FIG. 29A

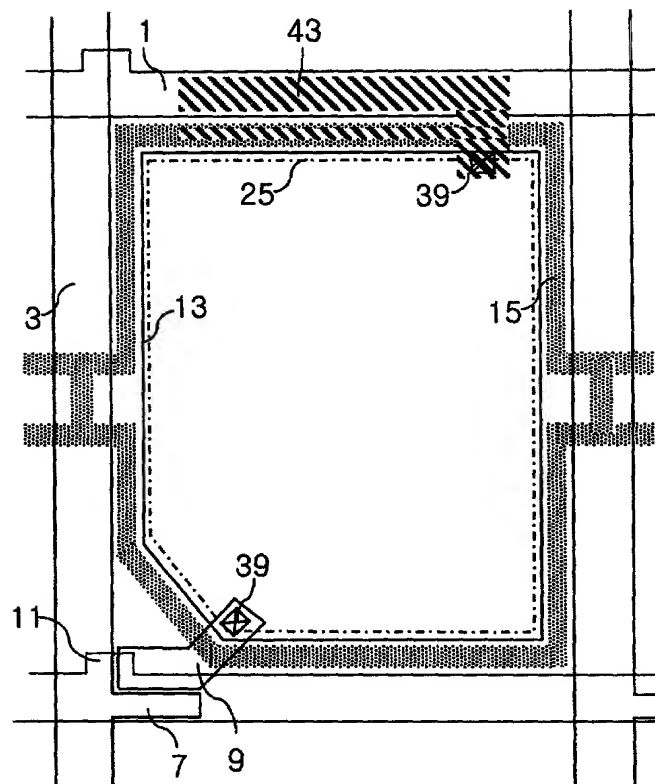
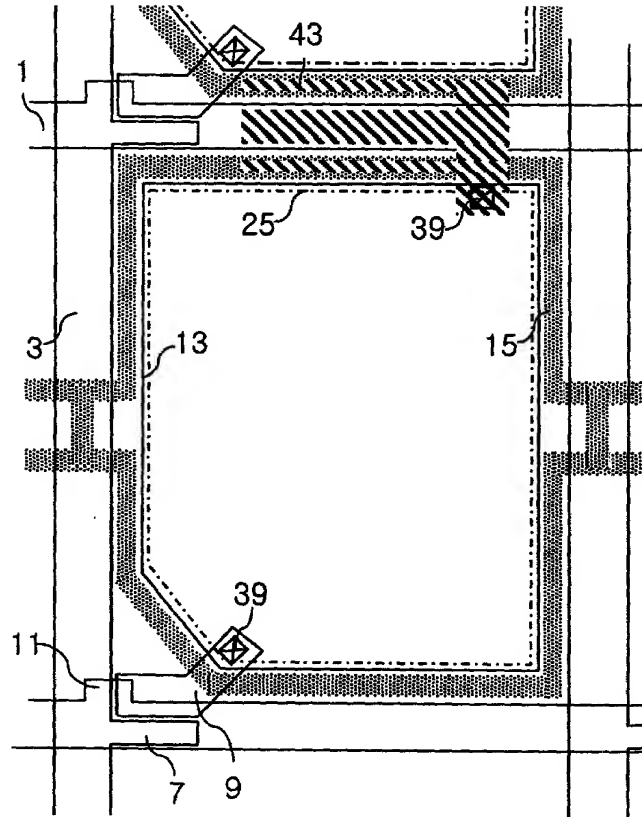




FIG. 29B



1



FIG. 30A

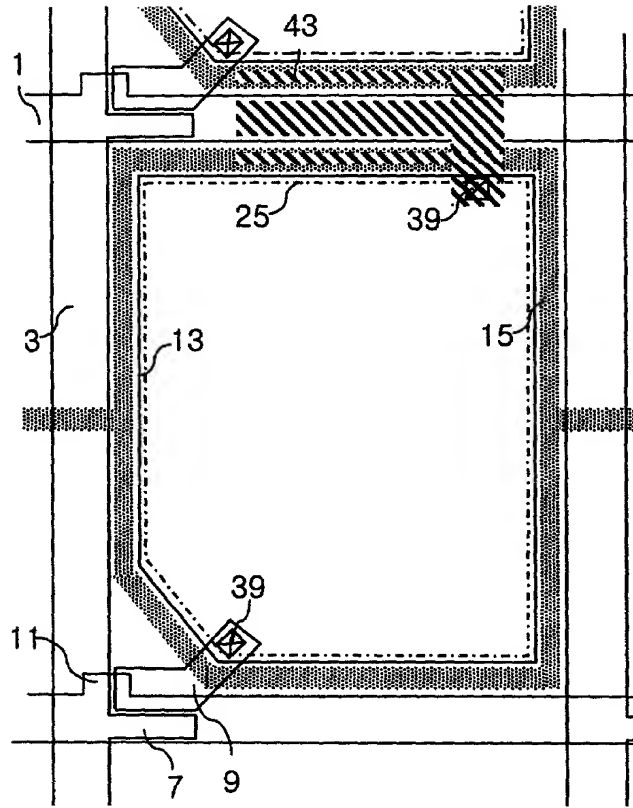


FIG. 30B

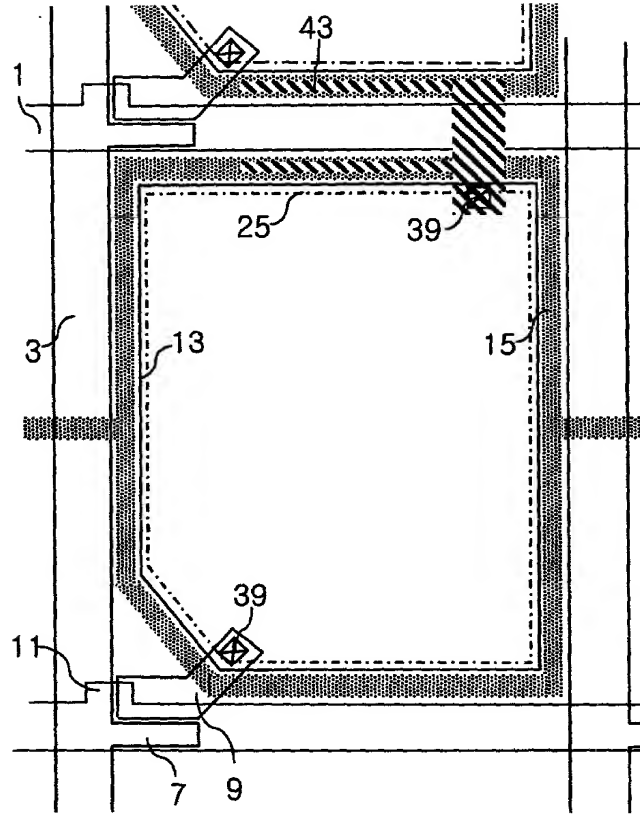


FIG. 31A

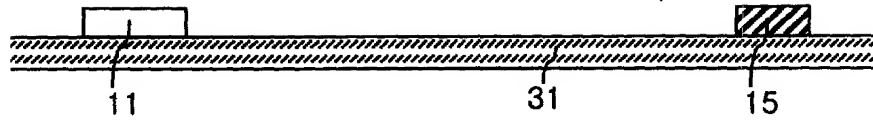


FIG. 31B

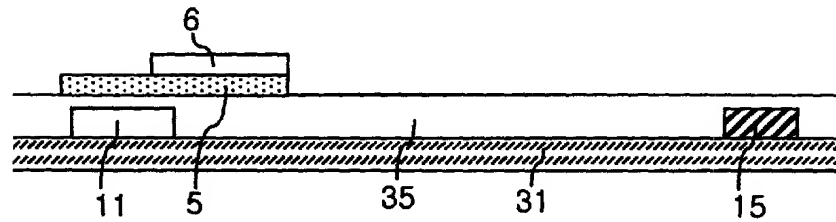


FIG. 31C

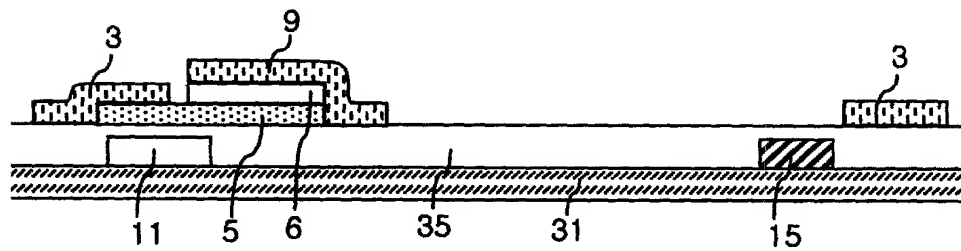


FIG. 31D

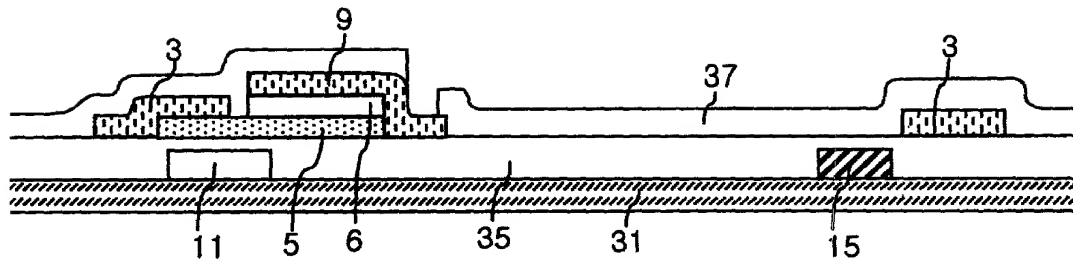


FIG. 31E

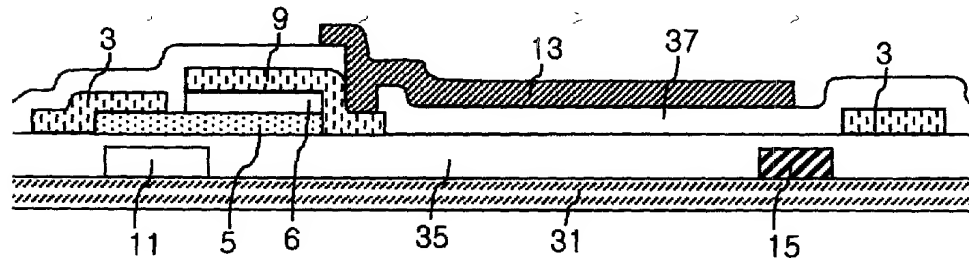


FIG. 32A

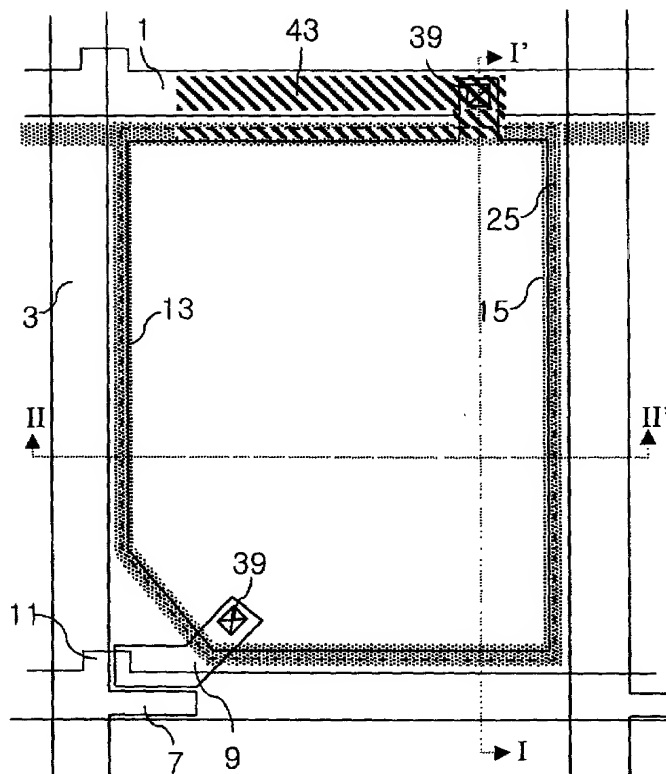


FIG. 32B

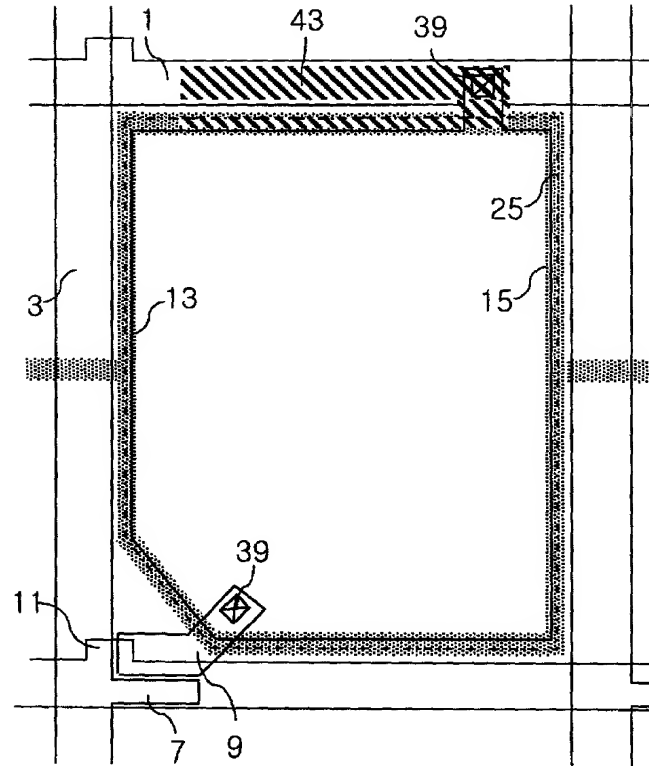


FIG. 32C

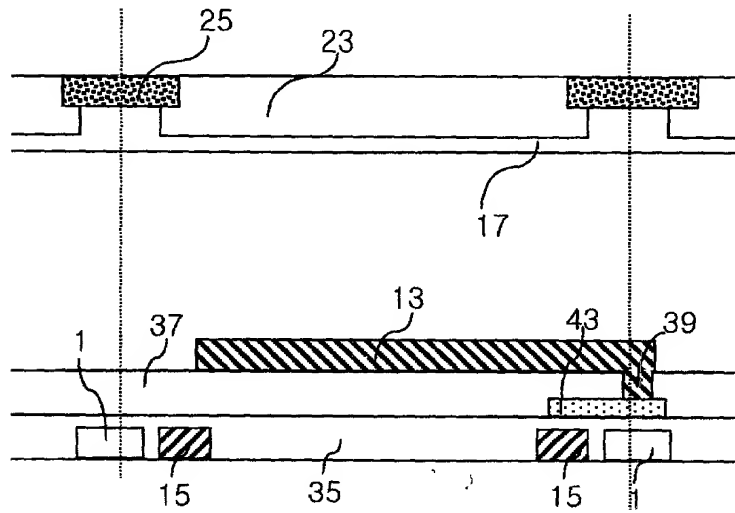




FIG. 32D

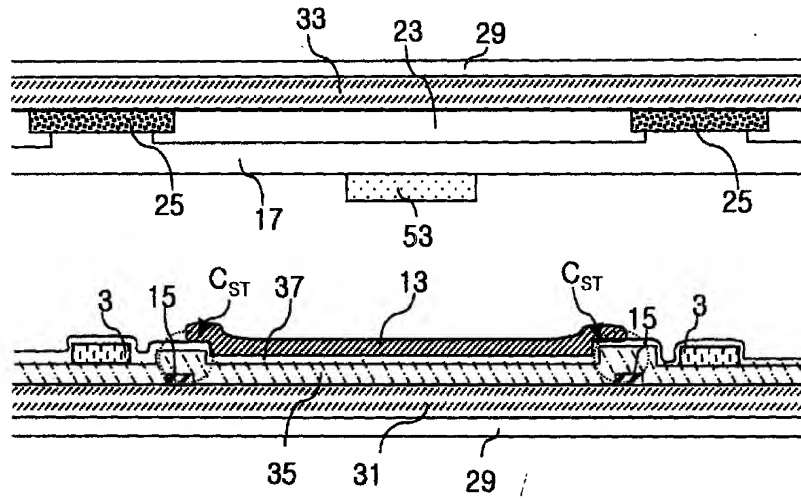


FIG. 32E

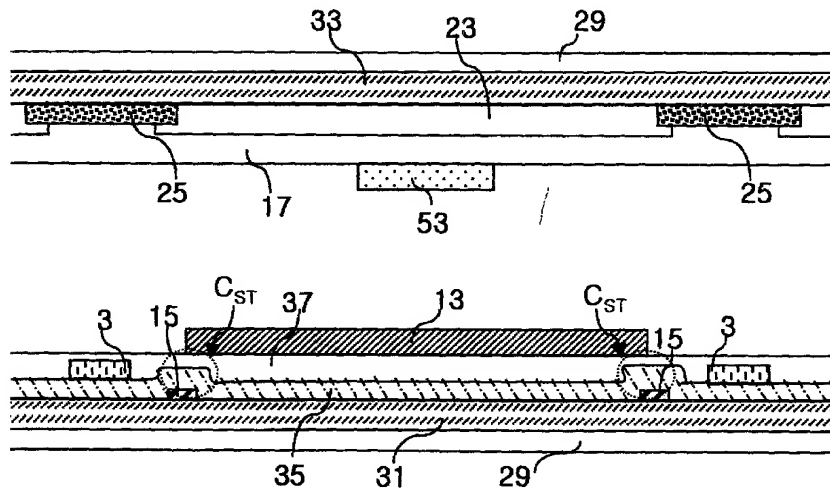


FIG. 32F

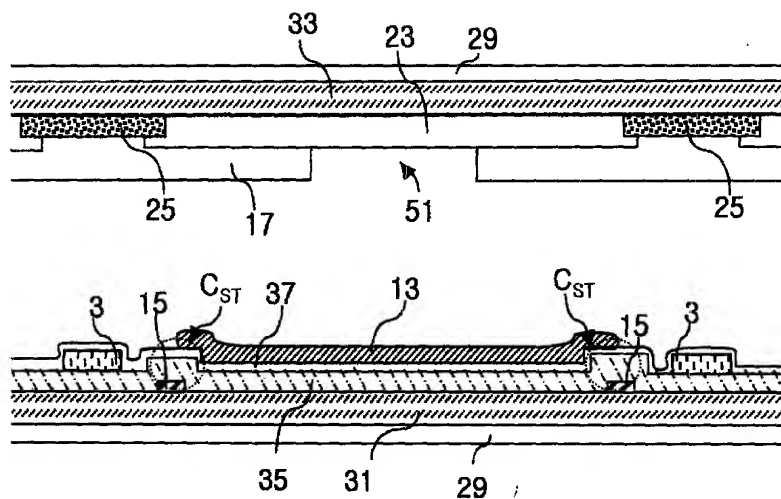
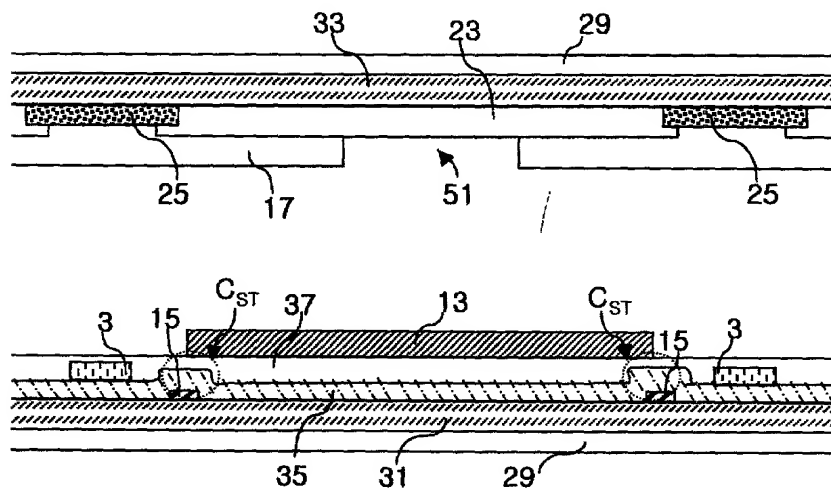


FIG. 32G



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FIG. 33A

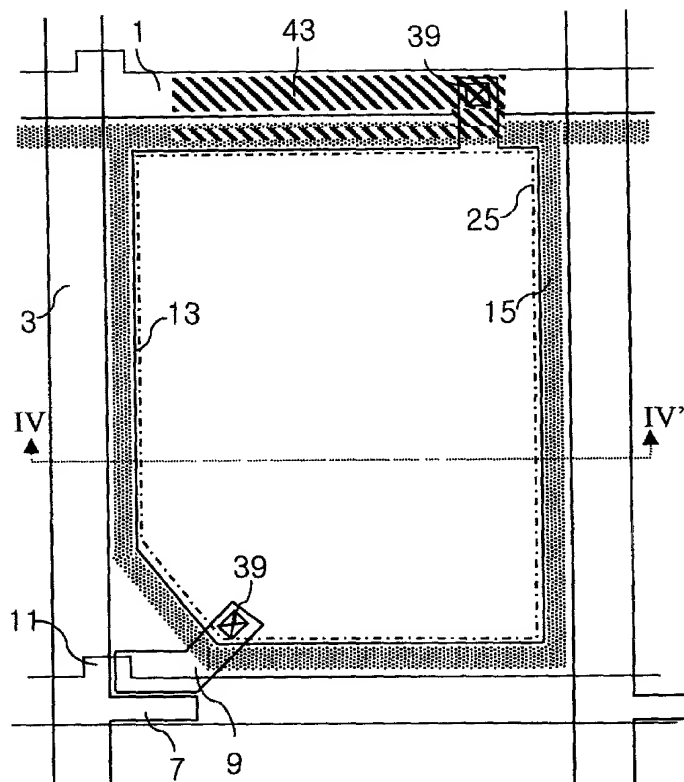


FIG. 33B

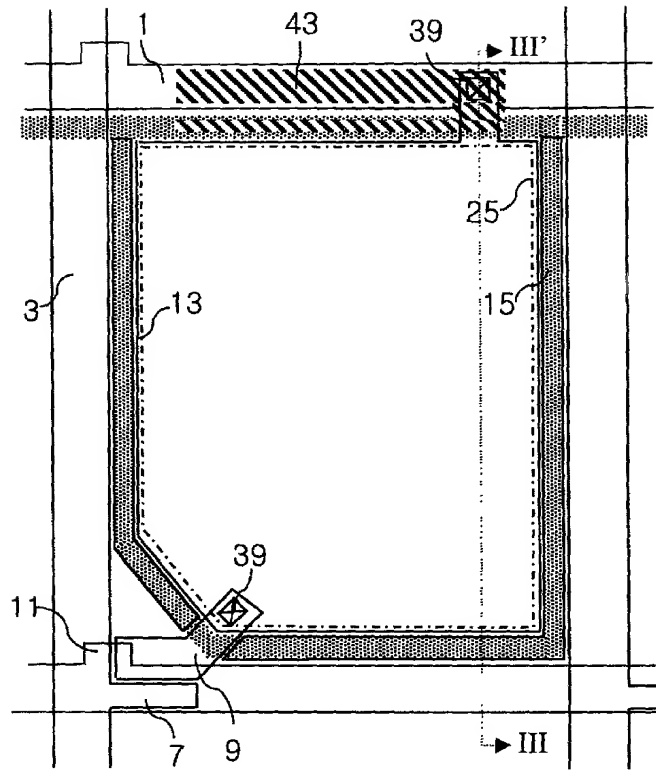


FIG. 33C

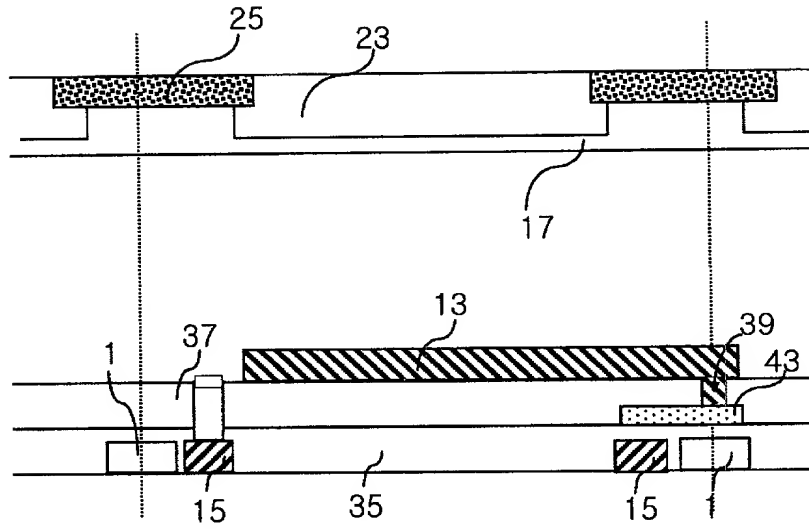


FIG. 33D

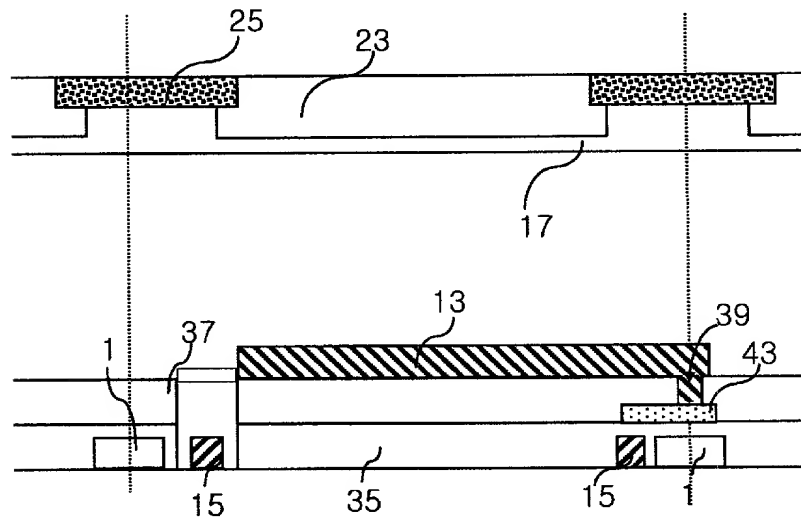


FIG. 33E

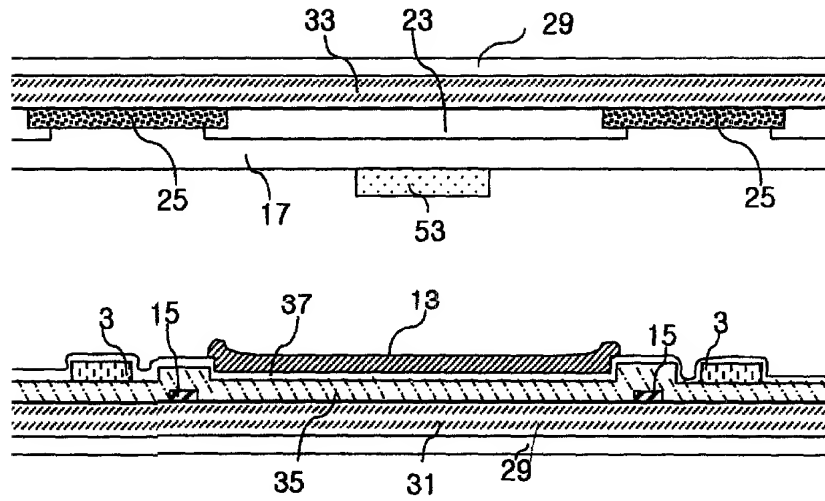


FIG. 33F

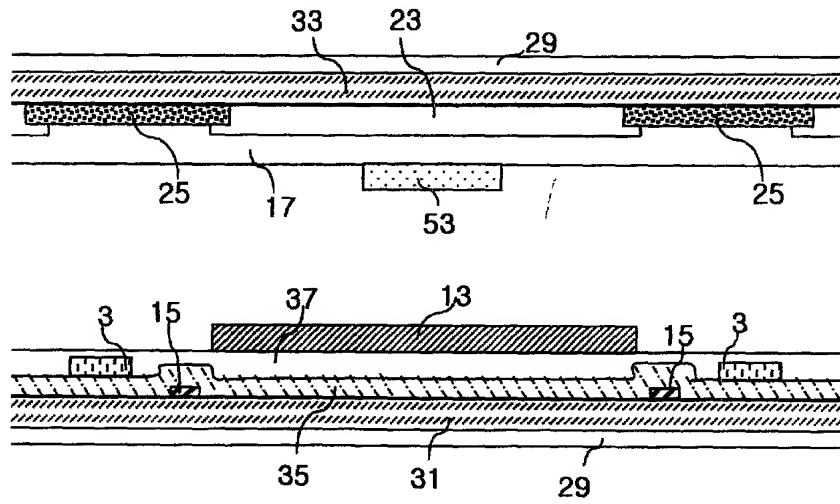


FIG. 33G

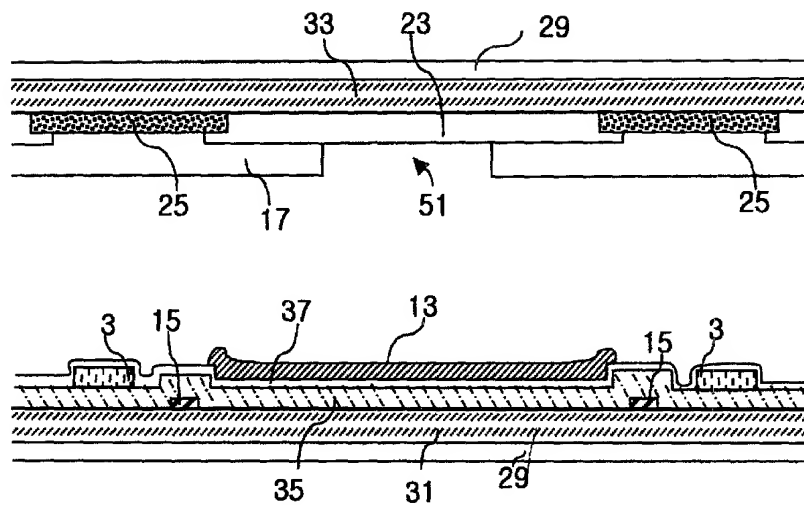


FIG. 33H

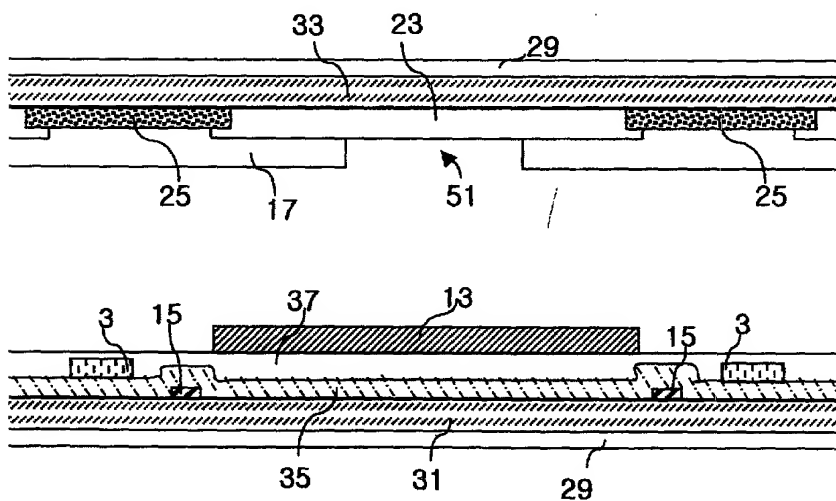






FIG. 35A

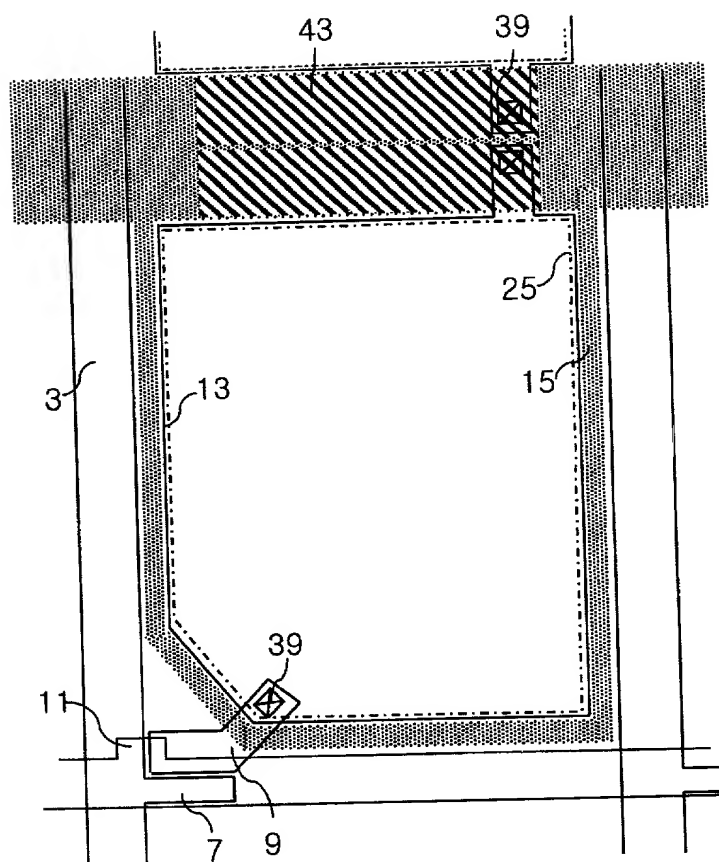


FIG. 35B

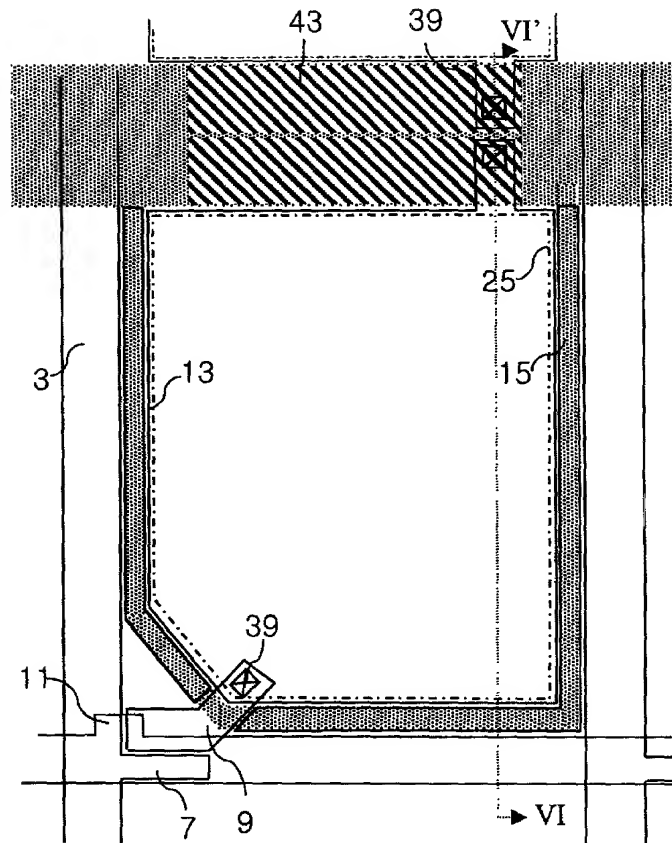


FIG. 35C

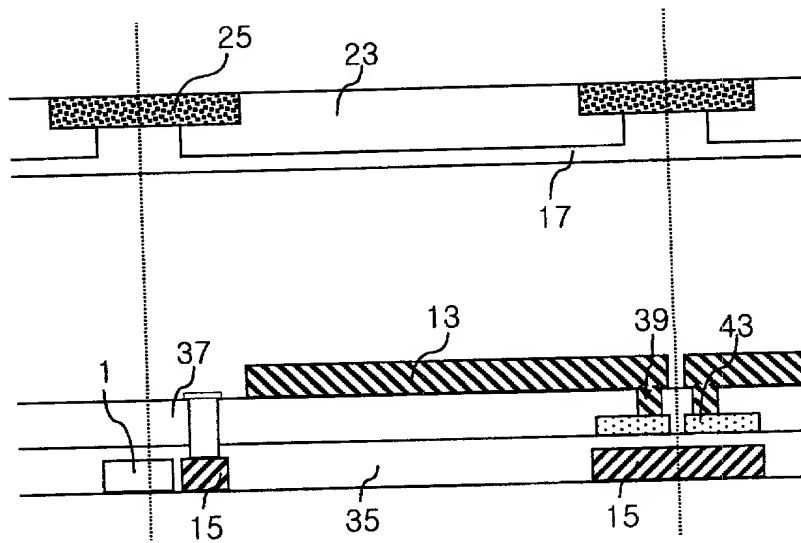


FIG. 35D

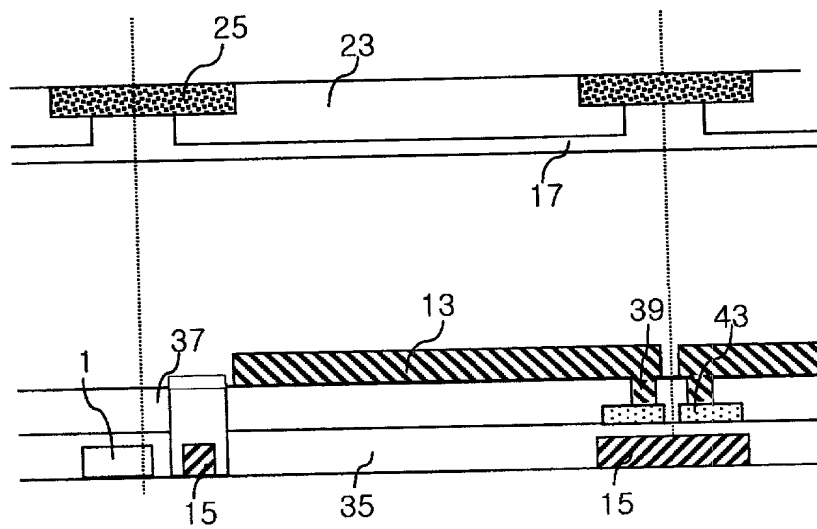


FIG. 36A

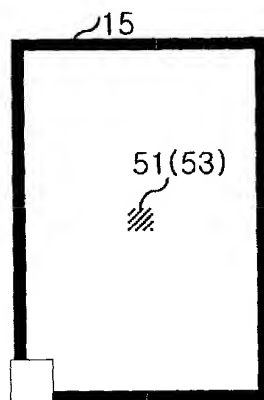


FIG. 36B

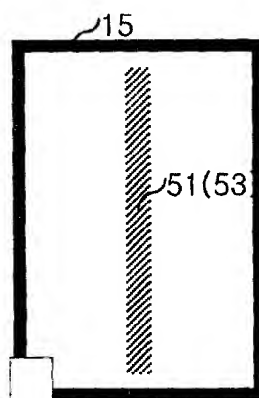


FIG. 36C

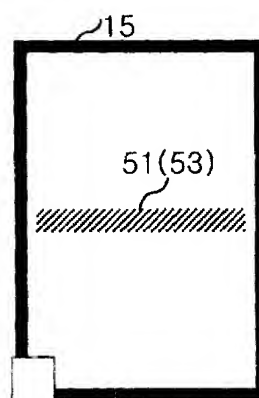


FIG. 36D

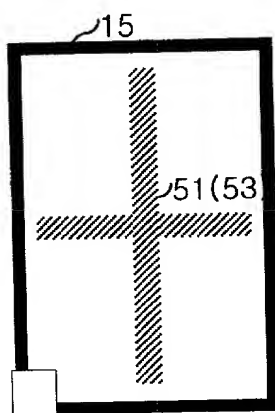


FIG. 36E

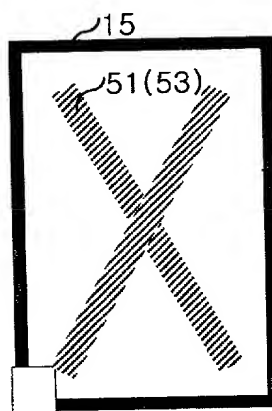


FIG. 36F

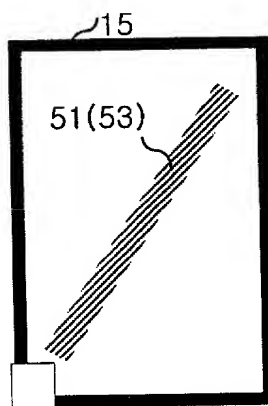


FIG. 36G

